

SUGHRUE, MICHENER, CREAK & SEAS, PLLC

JC14 Rec'd PCT/PTO 27 AUG 2001

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SCANNED # 6
1800 Pennsylvania Avenue, NW
Washington, DC 20037-3213

T 202.293.7060
F 202.293.7860

www.sughrue.com

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BOX PCT

Commissioner for Patents
Washington, D.C. 20231

PCT/JP00/00951
-filed February 21, 2000

Re: Application of Tadaaki HARADA, Hideaki TAKI, and
Toshitsugu HOSOKAWA
RESIN COMPOSITION FOR SEMICONDUCTOR ENCAPSULATION,
SEMICONDUCTOR DEVICE COMPRISING THE SAME AND PROCESS
FOR THE PRODUCTION OF SEMICONDUCTOR DEVICE USING THE
SAME
Assignee: NITTO DENKO CORPORATION
Our Ref: Q65847

Dear Sir:

The following documents and fees are submitted herewith in connection with the above application for the purpose of entering the National stage under 35 U.S.C. § 371 and in accordance with Chapter II of the Patent Cooperation Treaty:

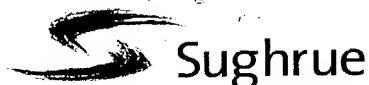
- ☒ an English translation of the International Application (167 pages, including the claims and Abstract).
- ☒ an English translation of an Amendment under Article 11 (corresponding to PCT Article 34) (2 pages)
- ☒ a substitute English language specification for examination, comprising the English translation of the International Application, into which the Article 11/ PCT Article 34 Amendments have been incorporated (167 pages including the claims and abstract) .
- ☒ an executed Declaration and Power of Attorney (attached to the substitute specification).
- ☒ an executed Assignment and PTO 1595 form.
- ☒ an International Search Report, a Form PTO-1449 listing the ISR references, and a complete copy of each reference.
- ☒ a copy of the Notification Concerning Submission or Transmittal of Priority Document.

It is assumed that copies of the International Preliminary Examination Report, and any Articles 19 and 34 amendments as required by § 371(c) will be supplied directly by the International Bureau, but if further copies are needed, the undersigned can easily provide them upon request.

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SUGHRUE MION ZINN MACPEAK & SEAS, PLLC

The Government filing fee (after entry of the Article 11 amendments) is calculated as follows:

Total claims	<u>22</u>	-	<u>20</u>	=	<u>2</u>	x	\$18.00	=	<u>\$36.00</u>
Independent claims	<u>1</u>	-	<u>3</u>	=	<u></u>	x	\$80.00	=	<u>\$0.00</u>
Base Fee									<u>\$860.00</u>
TOTAL FILING FEE									<u>\$896.00</u>
Recordation of Assignment									<u>\$ 40.00</u>
TOTAL FEE									<u>\$936.00</u>

Checks for the statutory filing fee of \$936.00 and Assignment recordation fee of \$40.00 are attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16, 1.17 and 1.492 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

Priority is claimed from February 25, 1999, February 25, 1999, February 25, 1999, July 27, 1999, July 27, 1999, July 27, 1999, and August 26, 1999 based on Japanese Application Nos. P. Hei. 11-048905, P. Hei. 11-048906, P. Hei. 11-048907, P. Hei. 11-212068, P. Hei. 11-212069, P. Hei. 11-212070, and P. Hei. 11-240511, respectively.

Since the anniversary of the first priority date fell on a Saturday, the filing of this application on Monday, August 27, 2001 is sufficient to obtain the benefit of priority.

Respectfully submitted,

Mark Boland

Registration No. 32,197

MXB:rw1

DESCRIPTION

RESIN COMPOSITION FOR SEMICONDUCTOR ENCAPSULATION,
SEMICONDUCTOR DEVICE COMPRISING THE SAME AND PROCESS
5 FOR THE PRODUCTION OF SEMICONDUCTOR DEVICE USING THE SAME

TECHNICAL FIELD

The present invention relates to an epoxy resin-based composition for semiconductor encapsulation which exhibits a
10 low viscosity at a temperature as relatively low as 80°C or lower and is excellent particularly in dischargeability and coatability as well as storage stability, a semiconductor device prepared by using the epoxy resin-based composition and a process for the production of the semiconductor device.

15

BACKGROUND ART

In order to encapsulate semiconductors at TAP (Tape Automated Bonding) process, COB (Chip On Board) process, etc., a liquid encapsulating medium has heretofore been used. Such
20 a liquid encapsulating medium is used at room temperature (25°C). By means of a dispenser or through printing process, semiconductor elements are encapsulated with such a resinous medium to produce semiconductor devices. As such a liquid encapsulating medium there has been generally known an epoxy
25 resin composition comprising a liquid epoxy resin, an acid anhydride-based curing agent, an ordinary curing accelerator and a silica powder.

The foregoing liquid encapsulating medium comprises an

(D) an inorganic filler.

The second essence of the invention lies in a semiconductor device comprising a semiconductor element provided on a wiring circuit board with a plurality of connecting electrode portions interposed therebetween, the gap between the wiring circuit board and the semiconductor element being sealed with an encapsulating resin layer, wherein the encapsulating resin layer is formed by the foregoing resin composition for semiconductor encapsulation.

The third essence of the invention lies in a process for the production of a semiconductor device comprising a semiconductor element provided on a wiring circuit board with a plurality of connecting electrode portions interposed therebetween, the gap between the wiring circuit board and the semiconductor element being sealed with an encapsulating resin layer, which comprises filling the gap between the wiring circuit board and the semiconductor element with the foregoing resin composition for semiconductor encapsulation, and then curing the resin composition to form the encapsulating resin layer.

The fourth essence of the invention lies in a semiconductor device comprising a semiconductor element provided on a wiring circuit board, the wiring circuit board and the semiconductor element being electrically connected to each other, the semiconductor element being encapsulated by an encapsulating resin layer in such an arrangement that the semiconductor element is incorporated therein, wherein the encapsulating resin layer is formed by the foregoing resin composition for semiconductor encapsulation.

The fifth essence of the invention lies in a process for the production of a semiconductor device comprising a semiconductor element provided on a wiring circuit board, the wiring circuit board and the semiconductor element being electrically connected to each other, the semiconductor element being encapsulated by an encapsulating resin layer in such an arrangement that the semiconductor element is incorporated therein, which comprises placing a semiconductor element on the wiring circuit board, electrically connecting the wiring circuit board and the semiconductor element to each other, supplying the foregoing resin composition for semiconductor encapsulation onto the wiring circuit board on the semiconductor element side thereof, and then curing the resin composition for semiconductor encapsulation to form the encapsulating resin layer.

The sixth essence of the invention lies in a semiconductor product comprising a semiconductor device having a resin encapsulation layer formed with a plurality of connecting electrode portions interposed therebetween, the semiconductor device being provided on a mounting board with the wiring circuit board of the semiconductor device being opposed to the mounting board and the gap between the mounting board and the semiconductor device being sealed with an encapsulating resin layer, wherein the encapsulating resin layer is formed by the foregoing resin composition for semiconductor encapsulation.

The seventh essence of the invention lies in a process for the production of a semiconductor device, which comprises a step of forming a resin layer made of the foregoing resin composition for semiconductor encapsulation on the protruding

electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon to a predetermined thickness in such an arrangement that at least the forward end of the protruding electrode portions is exposed to the exterior of the resin layer, and a step of cutting the semiconductor wafer having a resin layer formed thereon to individual semiconductor elements.

The eighth essence of the invention lies in a process for the production of a semiconductor device, which comprises a step of supplying the resin composition for semiconductor encapsulation onto the whole of a plurality of semiconductor elements provided on a matrix wiring circuit board having individual circuits formed thereon to form a resin layer with the semiconductor elements being incorporated therein, and a step of cutting the matrix wiring circuit board having a resin layer formed thereon with the semiconductor elements being incorporated therein together with the resin layer to every semiconductor element.

The ninth essence of the invention lies in a process for the production of a semiconductor device, which comprises a step of forming a resin layer made of the foregoing resin composition for semiconductor encapsulation on the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon to a predetermined thickness, a step of cutting the semiconductor wafer having a resin layer formed thereon to individual semiconductor elements, and a step of contact-bonding a wiring circuit board and the semiconductor element thus cut

an epoxy resin, a phenolic resin, a latent curing accelerator and an inorganic filler and having a predetermined viscosity at each of temperatures of 25°C and 80°C. Thus, the present invention has been worked out.

5 A resin composition for semiconductor encapsulation comprising as the latent curing accelerator a microcapsule type curing accelerator having a core-shell structure in which a core portion made of the curing accelerator is covered by a specific shell portion is advantageous in that it exhibits an extremely
10 prolonged pot life and thus is particularly excellent in storage stability.

Further, a resin composition for semiconductor encapsulation comprising as the inorganic filler a spherical fusedsilicapowderincorporatedthereininaspecificproportion
15 is advantageous in that it exhibits an excellent fluidity and thus is particularly excellent in dischargeability and coatability.

BEST MODE FOR CARRYING OUT THE INVENTION

Embodiments of implication of the present invention will
20 be described hereinafter in detail.

The resin composition for semiconductor encapsulation of the invention comprises an epoxy resin (component A), a phenolic resin (component B), a latent curing accelerator (component C) and an inorganic filler (component D) and exhibits a specific
25 viscosity at each temperature of 25°C and 80°C.

The resin composition for semiconductor encapsulation of the invention is roughly divided into three embodiments by the properties of the epoxy resin (component A) and phenolic resin

(component B) which are essential components thereof. The first embodiment of the resin composition for semiconductor encapsulation of the invention comprises a liquid epoxy resin (A1) as the epoxy resin (component A) and a solid phenolic resin (B1) as the phenolic resin (component B). The second embodiment of the resin composition for semiconductor encapsulation of the invention comprises a solid epoxy resin (A2) as the epoxy resin (component A) and a liquid phenolic resin (B2) as the phenolic resin (component B). The third embodiment of the resin composition for semiconductor encapsulation of the invention comprises a solid epoxy resin (A3) as the epoxy resin (component A) and a solid phenolic resin (B3) as the phenolic resin (component B). As the remaining components, i.e., latent curing accelerator (component C), inorganic filler (component D) and other additives there are used those common to the three embodiments.

The first embodiment of the resin composition for semiconductor encapsulation, which comprises a liquid epoxy resin (A1) as the epoxy resin (component A) and a solid phenolic resin (B1) as the phenolic resin (component B), will be further described hereinafter.

The liquid epoxy resin (A1) is not specifically limited so far as it stays liquid at a temperature of 25°C. Various epoxy resins may be used. Examples of these epoxy resins employable herein include bisphenol F type epoxy resin, bisphenol A type epoxy resin, alicyclic epoxy resin, and allylated bisphenol type epoxy resin. These epoxy resins may be used singly or in combination of two or more thereof. The term "liquid epoxy

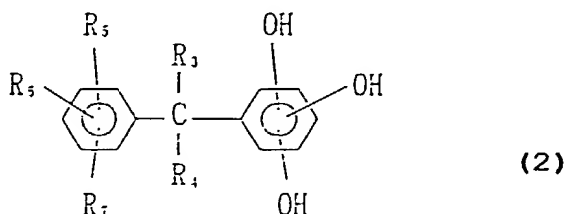
resin (component A1)" as used herein is meant to indicate not only an epoxy resin which stays liquid at 25°C when it is used singly but also an epoxy resin component which is composed of two or more of the epoxy resins and stays liquid at 25°C.

5 The foregoing liquid epoxy resin to be used herein preferably exhibits an epoxy equivalent of from 110 to 220 g/eq, particularly from 140 to 200 g/eq.

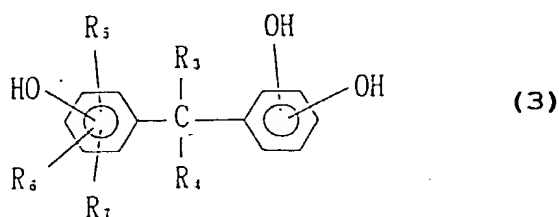
 The foregoing solid phenolic resin (component B1) is not specifically limited so far as it acts as a curing agent for
10 the foregoing liquid epoxy resin (component A1) and stays solid at a temperature of 25°C. Various phenolic resins may be used. These phenolic resins may be used in combination with acid anhydride-based curing agents such as tetrahydrophthalic anhydride, hexahydrophthalic anhydride,
15 methylhexahydrophthalic anhydride and phthalic anhydride, amines, phthalic acids, etc. so far as the desired object of the invention cannot be impaired. Examples of the solid phenolic resin (component B1) employable herein include polyfunctional solid phenolic resin, bisphenolic resin, naphthalene type
20 phenolic resin, phenol novolak resin, and triphenylmethane type phenolic resin. These solid phenolic resins may be used singly or in combination of two or more thereof. The term "polyfunctional solid phenolic resin" as used herein is meant to indicate a solid phenolic resin having at least one aromatic
25 ring containing two or more phenolic hydroxyl groups wherein the number of phenolic hydroxyl groups per molecule is 3 or more and there is contained two or more aromatic rings per molecule. Examples of the polyfunctional solid phenolic resin employable

herein include trifunctional solid phenolic resin, tetrafunctional solid phenolic resin, and pentafunctional solid phenolic resin. The polyfunctional solid phenolic resin, if used, preferably has a number-average molecular weight of 450 or less. The term "solid phenolic resin (component B1)" as used herein is meant to indicate not only a phenolic resin which stays solid at 25°C when it is used singly but also a phenolic resin component which is composed of two or more of the phenolic resins and stays solid at 25°C.

Among the foregoing solid phenolic resins (component B1), the trifunctional solid phenolic resin may be in the form of phenolic resin having a structure represented by the following general formula (2) or (3):

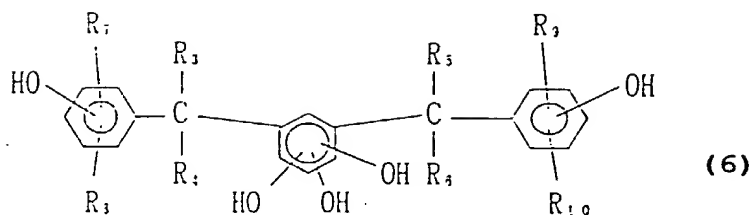


wherein R₃ to R₇ may be the same or different and each represent a hydrogen atom or methyl group.



wherein R₃ to R₇ are as defined in the general formula (2).

Among the foregoing solid phenolic resins (component B1),



wherein R_3 to R_{10} are as defined in the general formula (5).

The foregoing solid phenolic resin to be used herein preferably has a hydroxyl equivalent of from 30 to 260 g/eq and a softening point of from 40°C to 100°C or a melting point of from 50°C to 210°C, particularly a hydroxyl equivalent of from 50 to 110 g/eq and a softening point of from 60°C to 90°C or a melting point of from 70°C to 190°C. The softening point can be measured according to JIS K 7243.

The mixing proportion of the foregoing liquid epoxy resin (component A1) and solid phenolic resin (component B1) is preferably predetermined such that the amount of hydroxyl group in the phenolic resin per equivalent of epoxy group in the foregoing epoxy resin is from 0.6 to 1.4 equivalents, more preferably from 0.7 to 1.1 equivalents.

In the first embodiment, the foregoing liquid epoxy resin (component A1) and solid phenolic resin (component B1) to be combined are preferably a bisphenol F type epoxy resin and a polyfunctional solid phenolic resin from the standpoint of fluidity, heat resistance and curability.

The second embodiment of the resin composition for semiconductor encapsulation, which comprises a solid epoxy resin (A2) as the epoxy resin (component A) and a liquid phenolic resin

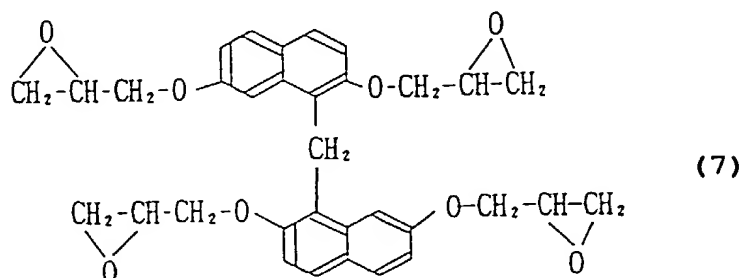
(B2) as the phenolic resin (component B), will be further described hereinafter.

The foregoing solid epoxy resin (component A2) is not specifically limited so far as it stays solid at a temperature of 25°C. Various epoxy resins may be used. Examples of the epoxy resins employable herein include polyfunctional solid epoxy resin, crystalline epoxy resin, bifunctional solid epoxy resin, and triglycidyl isocyanurate. These solid epoxy resins may be used singly or in combination of two or more thereof.

The term "polyfunctional solid epoxy resin" as used herein is meant to indicate a solid epoxy resin having three or more epoxy groups per molecule. Examples of the polyfunctional solid epoxy resin employable herein include tetrafunctional naphthalene type epoxy resin, and triphenylmethane type epoxy resin. The term "crystalline epoxy resin" as used herein is meant to indicate a solid epoxy resin which shows many crystalline peaks when subjected to X-ray diffractometry, physically shows a sharp melting point and exhibits a drastically lowered viscosity during melting because there is little or no intermolecular mutual interaction. Examples of the crystalline epoxy resin employable herein include bisphenol type epoxy resin, biphenyl type epoxy resin, and stilbene type epoxy resin. The term "solid epoxy resin (component A2)" as used herein is meant to indicate not only an epoxy resin which stays solid at 25°C when it is used singly but also an epoxy resin component which is composed of two or more of the epoxy resins and stays solid at 25°C.

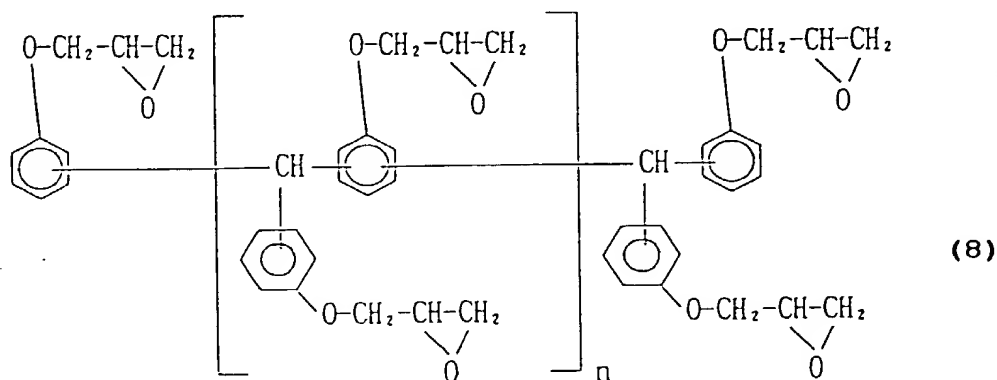
Among the foregoing solid epoxy resins (component A2), the tetrafunctional naphthalene type epoxy resin may be, e.g.,

Type EXA-4701 epoxy resin commercially available from DAINIPPON INK & CHEMICALS, INC. Type EXA-4701 epoxy resin is represented by the following structural formula (7):



10 Among the foregoing solid epoxy resins (component A2), the triphenylmethane type epoxy resin may be, e.g., Type EPPN-501HY epoxy resin commercially available from NIPPON KAYAKU CO., LTD. Type EPPN-501HY epoxy resin is represented by the following chemical formula (8):

15



25 wherein n represents 0 or a positive integer.

The foregoing solid epoxy resin to be used herein preferably has an epoxy equivalent of from 140 to 250 g/eq and

a softening point of from 50°C to 100°C or a melting point of from 45°C to 150°C, particularly an epoxy equivalent of from 150 to 220 g/eq and a softening point of from 60°C to 80°C or a melting point of from 50°C to 130°C.

5 The liquid phenolic resin (component B2) to be used in combination with the foregoing solid epoxy resin (component A2) is not specifically limited so far as it acts as a curing agent for the foregoing solid epoxy resin (component A2) and stays liquid at a temperature of 25°C. Various phenolic resins can
10 be used. Examples of the liquid phenolic resin employable herein include allylated phenol novolak, diallylated bisphenol A, acetylated phenol, and diallylbisphenol F. These liquid phenolic resins may be used singly or in combination of two or more thereof. The term "liquid phenolic resin (component B2)"
15 as used herein is meant to indicate not only a phenolic resin which stays liquid at 25°C when it is used singly but also a phenolic resin component which is composed of two or more of the phenolic resins and stays liquid at 25°C.

20 The foregoing liquid phenolic resin to be used herein preferably has a hydroxyl equivalent of from 80 to 200 g/eq, particularly from 100 to 170 g/eq.

 The mixing proportion of the foregoing solid epoxy resin (component A2) and liquid phenolic resin (component B2) is preferably predetermined such that the amount of hydroxyl group
25 in the phenolic resin per equivalent of epoxy group in the foregoing epoxy resin is from 0.7 to 1.4 equivalents, more preferably from 0.9 to 1.1 equivalents.

 In the second embodiment, the foregoing solid epoxy resin

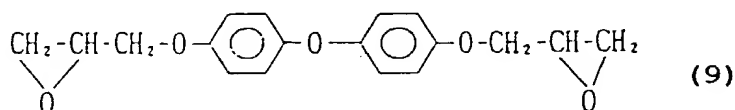
(component A2) and liquid phenolic resin (component B2) to be combined are preferably a tetrafunctional naphthalene type epoxy resin and an allylated phenol novolak from the standpoint of heat resistance and fluidity.

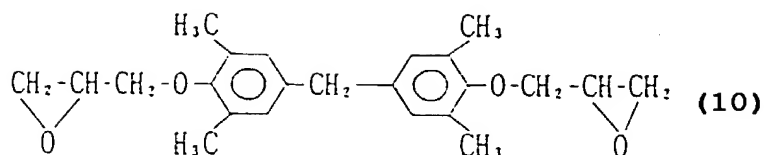
5 The third embodiment of the resin composition for semiconductor encapsulation, which comprises a solid epoxy resin (component A3) as the epoxy resin (component A) and a solid phenolic resin (component B3) as the phenolic resin (component B), will be further described hereinafter.

10 The foregoing solid epoxy resin (component A3) is not specifically limited so far as it stays solid at a temperature of 25°C. Various epoxy resins can be used. Examples of the solid epoxy resin employable herein include polyfunctional solid epoxy resin, crystalline epoxy resin, bifunctional solid epoxy
15 resin, triglycidyl isocyanurate, bisphenol F type epoxy resin, and bisphenol A type epoxy resin. These solid epoxy resins may be used singly or in combination of two or more thereof. The term "polyfunctional solid epoxy resin" as used herein is meant to indicate a solid epoxy resin having three or more epoxy groups
20 per molecule. Examples of the polyfunctional solid epoxy resin employable herein include tetrafunctional naphthalene type epoxy resin, triphenylmethane type epoxy resin, dicyclopentadiene type epoxy resin, Techmore VG3101L (produced by MITSUI CHEMICAL CORPORATION), and orthocresol novolak type
25 epoxy resin. The term "crystalline epoxy resin" as used herein is meant to indicate a solid epoxy resin which shows many crystalline peaks when subjected to X-ray diffractometry, physically shows a sharp melting point and exhibits a drastically

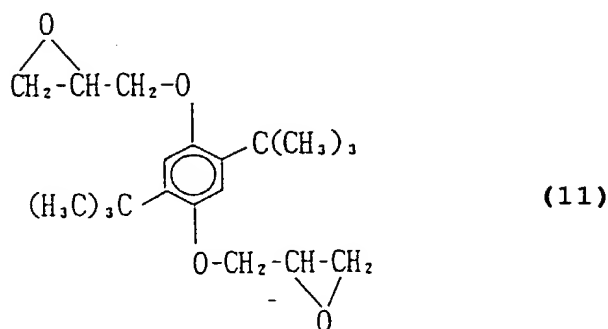
lowered viscosity during melting because there is little or no intermolecular mutual interaction. Examples of the crystalline epoxy resin employable herein include bisphenol type epoxy resin, biphenyl type epoxy resin, and stilbene type epoxy resin. When those having a melting point of 90°C or higher among these crystalline epoxy resins are used, two or more of these crystalline epoxy resins are preferably used for the reason that the resulting fluidity at a temperature of 80°C or lower is improved. The term "solid epoxy resin (component A3)" as used herein is meant to indicate not only an epoxy resin which stays solid at 25°C when it is used singly but also an epoxy resin component which is composed of two or more of the epoxy resins and stays solid at 25°C.

Among the foregoing solid epoxy resins (component A3), the crystalline epoxy resin may be, e.g., Type GK-4137 epoxy resin (commercially available from NIPPON STEEL CORP.), Type GK-5079 (commercially available from NIPPON STEEL CORP.), and Type YDC-1312 (commercially available from Tohto Kasei Corp.). Type GK-4137 epoxy resin is represented by the following chemical formula (9). Type GK-5079 epoxy resin is represented by the following chemical formula (10). Type YDC-1312 epoxy resin is represented by the following chemical formula (11).





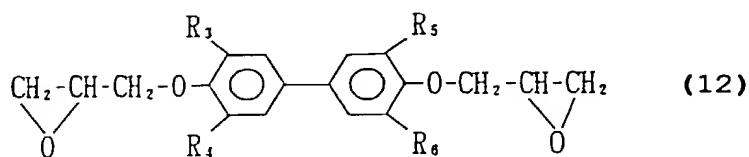
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Among the foregoing crystalline epoxy resins, the biphenyl type epoxy resin is represented by the following general formula (12) :

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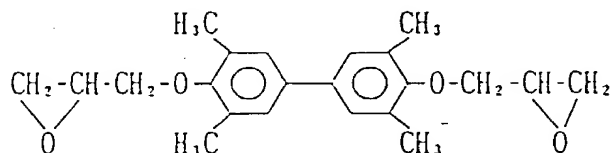
wherein R_3 to R_6 may be the same or different and each represent a hydrogen atom or C_{1-5} alkyl group.

Examples of the C_{1-5} alkyl group represented by R_3 to R_6 in the foregoing general formula (12) include straight-chain or branched lower alkyl group such as methyl group, ethyl group, n-propyl group, isopropyl group, n-butyl group, isobutyl group, sec-butyl group and tert-butyl group. R_3 to R_6 may be the same

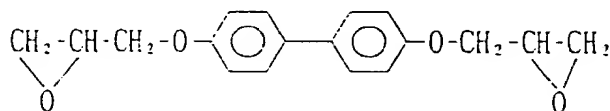
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or different.

As the foregoing biphenyl type epoxy resin there can be used a mixture of a biphenyl type epoxy resin having a structure represented by the following general formula (13) wherein all
 5 R_3 to R_6 are methyl group and a biphenyl type epoxy resin having a structure represented by the following general formula (14) wherein all R_3 to R_6 are hydrogen atom in almost the same amount:



(13)



(14)

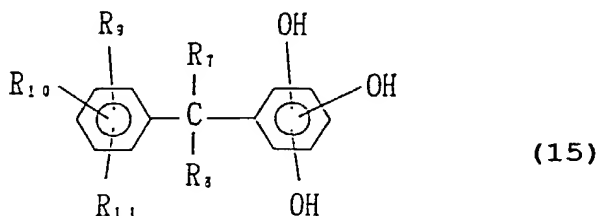
20 The foregoing solid epoxy resin to be used as component A3 preferably has an epoxy equivalent of from 140 to 270 g/eq and a softening point of from 50°C to 100°C or a melting point of from 40°C to 150°C, particularly an epoxy equivalent of from
 25 150 to 220 g/eq and a softening point of from 60°C to 80°C or a melting point of from 60°C to 130°C.

The foregoing solid phenolic resin (component B3) to be used in combination with the solid epoxy resin (component A3)

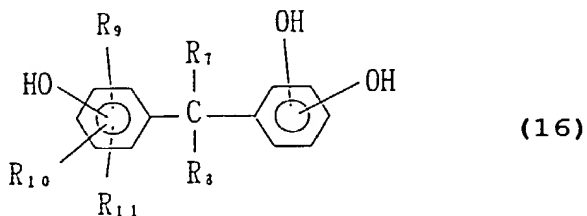
is not specifically limited so far as it stays solid at a temperature of 25°C. Various phenolic resins can be used. These phenolic resins may be used in combination with acid anhydride-based curing agents such as tetrahydrophthalic anhydride, hexahydrophthalic anhydride, methylhexahydrophthalic anhydride and phthalic anhydride, amines, phthalic acids, etc. so far as the desired object of the invention cannot be impaired. Examples of the solid phenolic resin (component B3) employable herein include polyfunctional solid phenolic resin, bisphenols, terphenols, terphenyldiphenols, dihydroxynaphthalenes, novolakphenol, allylated phenol, acetylated phenol, and triphenylmethane type phenolic resin. These solid phenolic resins may be used singly or in combination of two or more thereof. The term "polyfunctional solid phenolic resin" as used herein is meant to indicate a solid phenolic resin having at least one aromatic ring containing two or more phenolic hydroxyl groups wherein the number of phenolic hydroxyl groups per molecule is 3 or more and there is contained two or more aromatic rings per molecule. Examples of the polyfunctional solid phenolic resin employable herein include trifunctional solid phenolic resin, tetrafunctional solid phenolic resin, and pentafunctional solid phenolic resin. The polyfunctional solid phenolic resin, if used, preferably has a number-average molecular weight of 450 or less. The term "solid phenolic resin (component B3)" as used herein is meant to indicate not only a phenolic resin which stays solid at 25°C when it is used singly but also a phenolic resin component which is composed of two or more of the phenolic resins

and stays solid at 25°C.

Among the foregoing solid phenolic resins (component B3), the trifunctional solid phenolic resin may be, e.g., a phenolic resin having a structure represented by the following general formula (15) or (16):



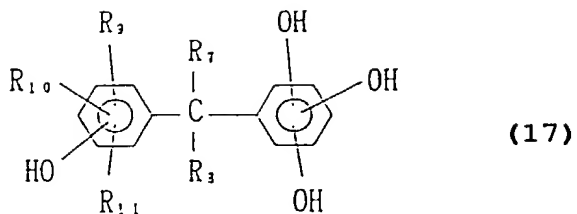
wherein R₇ to R₁₁ may be the same or different and each represent a hydrogen atom or methyl group.



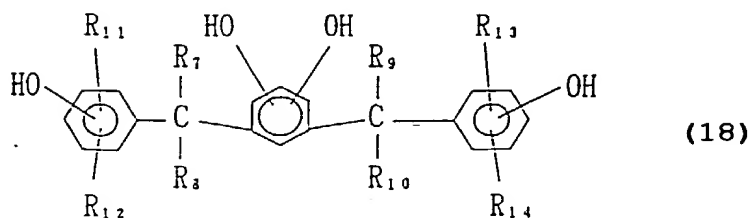
20 wherein R₇ to R₁₁ are as defined in the general formula (15).

Among the solid phenolic resins (component B3), the tetrafunctional solid phenolic resin can be, e.g., a phenolic resin having a structure represented by the following general formula (17) or (18):

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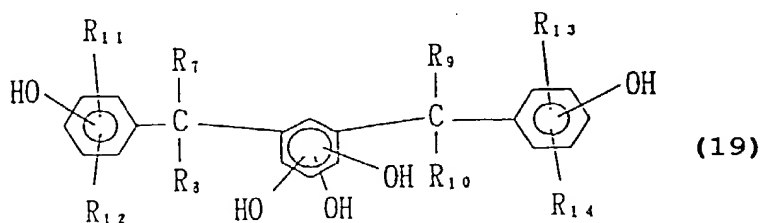


wherein R_7 to R_{11} are as defined in the general formula (15).



wherein R₇ to R₁₄ may be the same or different and each represent a hydrogen atom or methyl group.

Among the solid phenolic resins (component B3), the pentafunctional solid phenolic resin can be, e.g., a phenolic resin having a structure represented by the following general formula (19):



wherein R_7 to R_{14} are as defined in the general formula (18).

The foregoing solid phenolic resin to be used as the component B3 preferably has a hydroxyl equivalent of from 30

to 260 g/eq and a softening point of from 40°C to 100°C or a melting point of from 50°C to 210°C, particularly a hydroxyl equivalent of from 50 to 110 g/eq and a softening point of from 60°C to 90°C or a melting point of from 70°C to 190°C.

5 The mixing proportion of the foregoing solid epoxy resin (component A3) and solid phenolic resin (component B3) is preferably predetermined such that the amount of hydroxyl group in the phenolic resin per equivalent of epoxy group in the foregoing epoxy resin is from 0.6 to 1.4 equivalents, more
10 preferably from 0.7 to 1.1 equivalents.

The foregoing solid epoxy resin (component A3) and solid phenolic resin (component B3) to be combined are preferably a crystalline epoxy resin [particularly Type GK-4137 (produced by NIPPON STEEL CORP.)] and a polyfunctional solid phenolic resin
15 or tetrafunctional naphthalene type epoxy resin or triphenylmethane type epoxy resin and a bifunctional bisphenol resin from the standpoint of curability, heat resistance and fluidity.

The resin composition for semiconductor encapsulation of
20 the invention comprises a latent curing accelerator (component C) and an inorganic filler (component D) incorporated therein as essential components in combination with the various epoxy resins and phenolic resins according to the first to third embodiments.

25 When incorporated in the resin composition for semiconductor encapsulation, the foregoing latent curing accelerator (component C) acts to cause the resin composition to show a viscosity increase of 10 times or less after 72 hours

stay liquid at room temperature are suitable from the standpoint of workability in microencapsulation or the characteristics of the resulting microcapsules. The term "liquid at room temperature" as used herein is meant to indicate that the curing accelerator stays liquid at room temperature (25°C) itself or the curing accelerator stays solid at room temperature but can be liquefied by dissolving or dispersing in an arbitrary organic solvent.

Examples of the curing accelerator to be encapsulated include amine-based curing accelerator, imidazole-based curing accelerator, phosphorus-based curing accelerator, and phosphorus-boron-based curing accelerator. Specific examples of these curing accelerators include alkyl-substituted guanidines such as ethylguanidine, trimethylguanidine, phenylguanidine and diphenylguanidine, 3-substituted phenyl-1,1-dimethylureas such as 3-(3,4-dichlorophenyl)-1,1-dimethylurea, 3-phenyl-1,1-dimethylurea and 3-(4-chlorophenyl)-1,1-dimethylurea, 3-phenyl-1,1-dimethylurea and 3-(4-chlorophenyl)-1,1-dimethylurea, imidazolines such as 2-methylimidazoline, 2-phenylimidazoline, 2-undecylimidazoline and 2-heptadecylimidazoline, monoaminopyridines such as 2-aminopyridine, amineimides such as N,N-dimethyl-N-(2-hydroxy-3-allyloxypropyl)amine-N'-lactamide, organic phosphorus-based compounds such as ethylphosphine, propylphosphine, butylphosphine, phenylphosphine, trimethylphosphine, triethylphosphine,

cyclohexylene-1,4-diisocyanate, triisocyanates such as p-phenylenediisothiocyanate, xylylene-1,4-diisothiocyanate and ethylenediisothiocyanate, tetraisocyanates such as 4,4'-dimethyldiphenylmethane-2,2',5,5'-tetraisocyanate, 5 adduct of hexamethylenediisocyanate with hexanetriol, adduct of 2,4-tolylenediisocyanate with Brenzcatechol, adduct of tolylenediisocyanate with hexanetriol, adduct of tolylenediisocyanate with trimethylolpropane, adduct of xylylenediisocyanate with trimethylolpropane, adduct of 10 hexamethylenediisocyanate with trimethylolpropane, and isocyanate prepolymers such as trimer of aliphatic polyvalent isocyanate such as triphenyldimethylenetriisocyanate, tetraphenyltrimethylene tetraisocyanate, pentaphenyltetramethylene pentaisocyanate, lysine isocyanate 15 and hexamethylene diisocyanate. These compounds may be used singly or in combination of two or more thereof.

Preferred among the foregoing polyvalent isocyanates are isocyanate prepolymers represented by polymethylene polyphenyl isocyanates such as adduct of tolylene diisocyanate and 20 trimethylol propane, adduct of xylylene diisocyanate and trimethylolpropane and triphenyldimethylenetriisocyanate from the standpoint of film-forming properties in microencapsulization and mechanical strength.

On the other hand, as the polyvalent amines to be reacted 25 with the polyvalent isocyanates there can be used any compounds having two or more amino groups per molecule. Specific examples of these compounds employable herein include diethylenetriamine, triethylenetetramine, tetraethylene pentamine,

1,6-hexamethylenediamine, 1,8-octamethylene diamine,
 1,12-dodecamethylenediamine, o-phenylenediamine,
 m-phenylenediamine, p-phenylenediamine, o-xylylenediamine,
 m-xylylenediamine, p-xylylenediamine, menthanediamine,
 5 bis(4-amino-3-methylcyclohexyl)methane, isophoronediamine,
 1,3-diaminocyclohexane, and spiroacetal-based diamine. These
 compounds may be used singly or in combination of two or more
 thereof.

The reaction of the polyvalent isocyanates with water
 10 causes hydrolyzation of the polyvalent isocyanates resulting
 in the production of an amine that is then reacted with unreacted
 isocyanate groups (so-called self-polyaddition reaction) to
 produce a polymer comprising as a main component a polymer having
 a structural unit represented by the general formula (1).

15 As the polymer constituting the shell portion (wall
 membrane) there may be used a polyurethane-polyurea having a
 urethane bond in common with each other produced from the
 polyvalent isocyanate and a polyvalent alcohol in combination.

As the foregoing polyvalent alcohol there can be used any
 20 of aliphatic alcohol, aromatic alcohol and alicyclic alcohol.
 Examples of the polyvalent alcohol employable herein include
 catechol, resorcinol, 1,2-dihydroxy-4-methylbenzene,
 1,3-dihydroxy-5-methylbenzene,
 3,4-dihydroxy-1-methylbenzene,
 25 3,5-dihydroxy-1-methylbenzene, 2,4-dihydroxyethylbenzene,
 1,3-naphthalenediol, 1,5-naphthalenediol, 2,7-naphthalenediol,
 2,3-naphthalenediol, o,o'-biphenol, p,p'-biphenol, bisphenol
 A, bis-(2-hydroxyphenyl)methane, xylylenediol, ethyleneglycol,

1,3-propylene glycol, 1,4-butylene glycol, 1,5-pentanediol,
1,6-hexanediol, 1,7-heptanediol, 1,8-octanediol,
1,1,1-trimethylolpropane, hexanetriol, pentaerythritol,
glycerin, and sorbitol. These compounds may be used singly or
5 in combination of two or more thereof.

The foregoing microcapsule type curing accelerator can
be prepared through, e.g., the following three steps.

[1st step]

The curing accelerator as a core component is dissolved
10 or finely dispersed in a polyvalent isocyanate as a raw material
of wall (shell) to form an oil phase. Subsequently, the oil
phase thus formed is dispersed in the aqueous medium (aqueous
phase) containing a dispersion stabilizer in the form of oil
drop to prepare an O/W (oil phase/aqueous phase) emulsion.
15 Subsequently, in the O/W emulsion was dissolved a polyvalent
amine to cause interfacial polymerization of the polyvalent amine
with the polyvalent isocyanate in the oil phase to initiate
polyaddition reaction. Alternatively, the O/W emulsion is
heated so that the polyvalent isocyanate in the oil phase reacts
20 with water at the interface with the aqueous phase to produce
an amine that subsequently initiate self-polyaddition reaction.
Thus, microcapsules comprising as a shell portion (wall membrane)
a polyurea-based polymer, preferably a polyurea having a
structural unit represented by the general formula (1) are
25 prepared to obtain a microcapsule dispersion.

On the other hand, when a solid curing accelerator is
dissolved in an organic solvent to form a core component, an
S/O/W (solid phase/oil phase/aqueous phase) emulsion is obtained.

In this type of an emulsion, the curing accelerator is lipophilic. A hydrophilic curing accelerator can be difficultly formed into this type of an emulsion. In this case, however, the solubility of the curing accelerator can be properly adjusted to form an
 5 O/O (oil phase/oil phase) emulsion or S/O/O (solid phase/oil phase/oil phase) emulsion which then undergoes interfacial polymerization.

The organic solvent for dissolving the curing accelerator therein is not specifically limited so far as it stays liquid
 10 at room temperature. In practice, however, one which does not dissolve the shell portion (wall membrane) therein needs to be selected. Specific examples of the organic solvent employable herein include organic solvents such as ethyl acetate, methyl ethyl ketone, acetone, methylene chloride, xylene, toluene and
 15 tetrahydrofuran, and oils such as phenylxylylethane and dialkyl naphthalene.

[2nd step]

In the microcapsule dispersion obtained at the 1st step is then dissolved or dispersed a blocking agent. In this
 20 procedure, the addition of the blocking agent can be effected after the removal of the dispersion stabilizer or unreacted amine from the aqueous phase by centrifugal separation or the like to advantage.

[3rd step]

25 The microcapsule dispersion which has been subjected to blocking of amino group with a blocking agent at the 2nd step can be subjected to centrifugal separation, filtration or the like to remove excessive blocking agent, and then dried to prepare

membrane. Particularly preferred among these acidic compounds are formic acid and organic sulfonic acids.

The foregoing blocking agent is added in an amount equimolecular with the amino group present on and inside the wall membrane. In practical, when as the blocking agent there is used an acidic compound, the acidic material (acidic compound) is added to the dispersion which has just been subjected to microcapsulization (interfacial polymerization) so that the pH value of the dispersion is adjusted from basicity to acidity, preferably a pH of 2 to 5. Thereafter, the dispersion may be subjected to centrifugal separation, filtration or the like to remove excessive acidic compound.

In the process for the production of a microcapsule type curing accelerator comprising the foregoing 1st to 3rd steps, the 2nd step may also involve the passing of the microcapsule dispersion through an acidic cation exchange column by which the unreacted free amine can be removed or the remaining amino group can be neutralized.

The average particle diameter of the microcapsule type curing accelerator thus obtained is not specifically limited. In practice, however, it is preferably predetermined to a range of from 0.05 to 500 μm , more preferably from 0.1 to 30 μm from the standpoint of uniformity in dispersion. The shape of the microcapsule type curing accelerator is preferably sphere but may be ellipsoid. In the case where the microcapsules are not uniform in particle diameter, e.g., where the microcapsules are ellipsoidal or flat rather than spherical, the value obtained by simply averaging the longest diameter and the shortest

diameter is defined as average particle diameter.

The amount of the curing accelerator to be encapsulated in the microcapsule type curing accelerator is preferably predetermined to be from 10 to 95% by weight, particularly from 30 to 80% by weight based on the total amount of microcapsules. In other words, when the amount of the curing accelerator to be encapsulated falls below 10% by weight, the curing reaction time is too long to give sufficient reactivity. On the contrary, when the amount of the curing accelerator to be encapsulated exceeds 95% by weight, the thickness of the resulting wall membrane is too small to provide the core portion (curing agent) with sufficient isolatability and mechanical strength.

The ratio of the thickness of the shell portion (wall membrane) to the particle diameter of the microcapsule type curing accelerator is preferably predetermined to a range of from 3 to 25%, particularly from 5 to 25%. In other words, when the foregoing ratio falls below 3%, the resulting microcapsules cannot be provided with a sufficient mechanical strength against the shear developed at the kneading step during the production of epoxy resin composition. On the contrary, when the foregoing ratio exceeds 25%, there is a tendency that the curing accelerator encapsulated is insufficiently released.

The amount of the latent curing accelerator (component C) to be incorporated is preferably predetermined to a range of from 0.1 to 40 parts by weight (hereinafter abbreviated as "parts"), particularly from 5 to 20 parts based 100 parts of the foregoing embodiments of phenolic resin (components B1 to B3). In other words, when the amount of the latent curing

oxide, beryllium oxide, silicon carbide, silicon nitride, and aerogil. These compounds may comprise an electrically-conductive particulate material such as nickel, gold, copper, silver, tin, lead and bismuth incorporated therein.

5 Preferred among these compounds is spherical silica powder, particularly spherical fused silica powder. The average particle diameter of the inorganic filler is preferably from 0.01 to 60 μm , more preferably from 0.1 to 15 μm . The term "spherical" as used herein is meant to indicate that the
10 sphericity is 0.85 or more on the average as measured by a Type FPIA-100 flow type particle image analyzer (produced by SYSMEX INC.).

The content of the inorganic filler (component D) is preferably predetermined to a range of from 15 to 85%,
15 particularly from 50 to 80% based on the total weight of the resin composition for semiconductor encapsulation. In other words, when the content of the inorganic filler falls below 15% by weight, the resulting resin composition for semiconductor encapsulation tends to exhibit a reduced viscosity at 25°C and
20 thus undergoes sedimentation of inorganic filler during storage or exhibit a high hygroscopicity and hence a deteriorated reliability in humidity resistance. On the contrary, when the content of the inorganic filler exceeds 85%, the resulting resin composition for semiconductor encapsulation tends to exhibit
25 a reduced fluidity and hence deteriorated dischargeability and coatability.

Further, the resin composition for semiconductor encapsulation of the invention may comprise other additives

incorporated therein besides the foregoing essential components as necessary.

Examples of the other additives employable herein include fire retardant, wax, leveling agent, anti-foaming agent, pigment, dye, silane coupling agent, and titanate-based coupling agent.

Examples of the silane coupling agent employable herein include γ -mercaptopropyl trimethoxysilane, γ -glycidoxypentyl methyl diethoxysilane,

γ -methacryloxypropyl trimethoxysilane, and amino group-containing silane. These silane coupling agents may be used singly or in combination of two or more thereof.

Examples of the fire retardant employable herein include novolak type bromated epoxy resin, and bromated bisphenol A type epoxy resin, metallic compounds such as antimony trioxide, antimony pentoxide, magnesium hydroxide and aluminum hydroxide, and phosphorus-based compounds such as red phosphorus and ester phosphate. These fire retardants may be used singly or in combination of two or more thereof.

Examples of the wax employable herein include a higher fatty acid, a higher fatty acid ester, a higher fatty acid calcium, and an amide-based compound. These waxes may be used singly or in combination of two or more thereof.

The resin composition for semiconductor encapsulation of the invention may comprise a silicone oil, silicone rubber, synthetic rubber or reactive diluent incorporated therein besides the above-described other additives to reduce the resulting stress. The resin composition for semiconductor

coatability and thus cannot satisfy the desired requirements.

The viscosity of the resin composition for semiconductor encapsulation at 25°C and 80°C can be measured by an E type viscometer as follows.

5 [Viscosity at 25°C]

For the measurement of viscosity at 25°C, a Type RE80U viscometer comprising a cone rotor having a size of 3° and R7.7 produced by Toki Sangyo Co., Ltd. was used. The sample was pretreated at a cone rotor rotary speed of 1 rpm for 1 minute
10 at 25°C, and then allowed to stand at 0.1 rpm for 10 minutes at 25°C before measurement.

[Viscosity at 80°C]

For the measurement of viscosity at 80°C, a Type RE80R viscometer produced by Toki Sangyo Co., Ltd. was used. For the
15 sample having a viscosity of less than 1,000 poise, a cone rotor having a size of 3° x R14 was used. For the sample having a viscosity of 1,000 poise or more, a cone rotor having a size of 3° x R7.7 was used. The sample was pretreated at a cone rotor rotary speed of 1 rpm for 1 minute at 80°C, and then allowed
20 to stand at 0.5 rpm for 10 minutes at 80°C before measurement.

The production of a semiconductor device from the resin composition for semiconductor encapsulation of the invention is accomplished by various known methods. In mounting by flip chip, COB, graph top, cavity fill, etc., the resin composition
25 for semiconductor encapsulation which has been heated (to a temperature of from about 40°C to 90°C, preferably from about 60°C to 80°C) is supplied by means of a dispenser so that the semiconductor element is potted. The resin composition is then

Among the foregoing processes for the production of a semiconductor device, flip chip mounting will be further described hereinafter with reference to side fill encapsulation process, press bump encapsulation process and printing encapsulation process.

[Side fill encapsulation process]

A wiring circuit board having semiconductor elements provided thereon with a plurality of connecting electrode portions interposed therebetween is prepared. The gap between the wiring circuit board and semiconductor elements which have been previously heated (to a temperature of from about 40°C to 130°C, preferably from 60°C to 100°C) is then filled with the resin composition for semiconductor encapsulation which has been heated (to a temperature of from about 40°C to 90°C, preferably from about 60°C to 80°C) and injected by means of a dispenser. The resin composition thus injected is then heated and cured to form an encapsulating resin layer. In this manner, a semiconductor device can be produced by flip chip mounting.

Alternatively, a solid or semi-solid resin composition for semiconductor encapsulation which has not previously been heated may be directly applied onto the semiconductor elements

or to the area in the vicinity thereof, and then heated and cured to form an encapsulating resin layer in the gap between the semiconductor elements and the wiring circuit board.

The production of a semiconductor by side fill encapsulation process may be effected in vacuo. As the apparatus for the production of such a semiconductor device in vacuo there may be used a Type MBC-V Series produced by Musashi Engineering Co., Ltd. The foregoing production of a semiconductor device in vacuo may be accomplished by differential pressure filling method which comprises filling of the gap between the wiring circuit board and the semiconductor elements in vacuo with the resin composition for semiconductor encapsulation which has been injected by means of a dispenser and subsequent filling the gap with the resin composition at atmospheric pressure.

[Press bump encapsulation process]

The resin composition for semiconductor encapsulation which has been heated (to a temperature of from about 40°C to 90°C, preferably from 60°C to 80°C) is applied onto the wiring circuit board by means of a dispenser so that the semiconductor elements are potted. Thereafter, press bump connection process using a flip chip bonder or the like is effected to carry out the electrical connection between the semiconductor elements and the wiring circuit board and the formation of an encapsulating resin layer at the same time. In this manner, a semiconductor device can be produced by flip chip mounting.

Alternatively, a solid or semi-solid resin composition for semiconductor encapsulation which has not previously been heated may be directly applied to the semiconductor elements

or wiring circuit board, followed by press bump connection process. In this manner, the connection between the semiconductor elements and the wiring circuit board and the formation of an encapsulating resin layer can be effected at
5 the same time.

The production of a semiconductor device by press bump encapsulation process may be effected in vacuo.

Instead of potting by means of a dispenser, printing may be effected to apply the resin composition if possible, followed
10 by press bump connection process by flip chip bonder or the like which effects the electrical connection between the semiconductor elements and the wiring circuit board and the formation of an encapsulating resin layer at the same time. In the application by printing, the printing atmosphere may be
15 entirely heated or the mask or squeegee may be partly heated (to a temperature of from about 40°C to 100°C).

[Printing encapsulation process]

A wiring circuit board having semiconductor elements provided thereon with a plurality of connecting electrode
20 portions interposed therebetween is prepared. The resin composition for semiconductor encapsulation which has been heated (to a temperature of from about 40°C to 90°C, preferably from about 60°C to 80°C) is then dropped into the gap between the wiring circuit board and semiconductor elements which has
25 previously been heated (to a temperature of from about 40°C to 130°C, preferably from about 60°C to 100°C) by means of a dispenser so that printing encapsulation is effected to form an encapsulating resin layer. In this manner, a semiconductor

device can be produced by flip chip mounting.

The printing encapsulation process is preferably accomplished by the use of a Type VPE-100 Series vacuum printing encapsulation apparatus using vacuum differential pressure
 5 (produced by Toray Engineering Co., Ltd.) because air bubbles can hardly enter into the encapsulating resin layer.

Alternatively, a solid or semi-solid resin composition for semiconductor encapsulation which has not previously been heated may be directly applied to a stage or squeegee so that
 10 printing encapsulation can be effected.

On the other hand, among the foregoing processes for the production of a semiconductor device, the cavity fill process for the production of a semiconductor device will be further described hereinafter.

15 A wiring circuit board having semiconductor elements provided thereon in such an arrangement that the semiconductor elements are electrically connected thereto through a bonding wire or the like is prepared. The resin composition for semiconductor encapsulation which has been heated (to a
 20 temperature of from about 40°C to 90°C, preferably from about 60°C to 80°C) is then applied to the wiring circuit board and semiconductor elements which has previously been heated (to a temperature of from about 40°C to 130°C, preferably from about 60°C to 100°C) by means of a dispenser so that the semiconductor
 25 elements are potted. The resin composition is then heated and cured so that an encapsulating resin layer is formed to encapsulate the semiconductor elements. In this manner, a cavity fill type of semiconductor device can be produced.

Alternatively, a solid or semi-solid resin composition for semiconductor encapsulation which has not previously been heated may be directly adhered or applied to the semiconductor elements, and then heated and cured to form an encapsulating resin layer in such an arrangement that the semiconductor elements are encapsulated.

The production of a semiconductor device by the foregoing encapsulation method may be effected in vacuo. As the apparatus for the production of such a semiconductor device in vacuo there may be used a Type MBC-V Series produced by Musashi Engineering Co., Ltd.

Other processes for the production of a semiconductor device will be further described hereinafter. A wiring circuit board having semiconductor elements provided thereon in such an arrangement that the semiconductor elements are electrically connected thereto through a bonding wire or the like is prepared. The resin composition for semiconductor encapsulation which has been heated (to a temperature of from about 40°C to 90°C, preferably from about 60°C to 80°C) is then supplied on the wiring circuit board and semiconductor elements which have previously been heated (to a temperature of from about 40°C to 130°C, preferably from 60°C to 100°C) by printing or the like, and then heated and cured to form an encapsulating resin layer in such an arrangement that the semiconductor elements are encapsulated. In this manner, a cavity fill type of semiconductor device can be produced.

The production of a semiconductor device by printing encapsulation process may be effected in vacuo. In the procedure

of producing a semiconductor device in vacuo, printing encapsulation in vacuo may be followed by the rise in the atmospheric pressure that causes the removal of voids from the resin composition for semiconductor encapsulation. Finish
5 printing may be then effected under these conditions.

The semiconductor device is then placed on a mounting board (mother board) to produce a semiconductor product. In some detail, the semiconductor device is mounted on the mounting board (mother board) with a plurality of connecting electrode portions
10 being provided interposed therebetween and its wiring circuit board being opposed to the mounting board. At the same time, the gap between the mounting board and the semiconductor device is filled with the resin composition for semiconductor encapsulation of the invention which is then heated and cured
15 to form an encapsulating resin layer. In this manner, a semiconductor product is produced.

The method for heating and curing the resin composition for semiconductor encapsulation is not specifically limited. In practice, however, a heating method using a convection dryer,
20 IR reflow furnace, hot plate or the like can be used.

Examples of the method for filling the gap between the mounting board and the semiconductor device with the resin composition for semiconductor encapsulation of the invention include the same methods as described with flip chip mounting
25 among the processes for the production of a semiconductor device mentioned above, i.e., side fill encapsulation process, press bump encapsulation process, printing encapsulation process, etc. The resin composition for semiconductor encapsulation may

The production of a semiconductor device will be described with reference to the case where the resin composition for semiconductor encapsulation of the invention is applied to a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon. In some detail, the resin composition for semiconductor encapsulation which has been heated (to a temperature of from about 40°C to 90°C, preferably from about 60°C to 80°C) is applied to the protruding electrode portion side of the semiconductor wafer by means of a dispenser to form a resin layer made of the resin composition for semiconductor encapsulation to a predetermined thickness. The formation of a resin layer made of the resin composition for semiconductor encapsulation to a predetermined thickness is effected in such a manner that at least the forward

end of the protruding electrode portion is exposed to the exterior of the resin layer. Subsequently, the semiconductor wafer having a resin layer formed thereon is cut to produce a semiconductor device.

5 The formation of a resin layer made of the resin composition for semiconductor encapsulation can be carried out by printing through an opening in the mask.

 The resin layer thus formed may be in any state so far as it is eventually thermoset. The heating and curing of the
10 resin layer may be effected before or after cutting of the semiconductor wafer.

 On the other hand, the resin composition for semiconductor encapsulation is supplied onto the whole of a plurality of semiconductor elements mounted on a matrix wiring circuit board
15 having individual wiring circuits formed thereon to form a resin layer in such an arrangement that the semiconductor elements are encapsulated. Subsequently, the resin layer is thermoset to encapsulate the plurality of semiconductor elements with the resin composition. The plurality of semiconductor elements
20 thus encapsulated are then cut every semiconductor element to produce semiconductor devices.

 The resin layer thus formed may be in any state so far as it is eventually thermoset. The heating and curing of the resin layer may be effected before or after cutting of the
25 semiconductor elements.

 As the method for forming the resin layer made of the resin composition for semiconductor encapsulation there may be used a method using a dispenser, a method involving printing through

an opening in the mask or the like similarly to the method previously mentioned.

Alternatively, the resin composition for semiconductor encapsulation is supplied onto the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon to form a resin layer to a predetermined thickness. The semiconductor wafer having a resin layer formed thereon is then cut to individual semiconductor elements. Subsequently, the wiring circuit board and the semiconductor elements are contact-bonded to each other under heating with the resin layer side of the semiconductor elements thus separated and the plurality of wiring circuit boards being opposed to each other so that they are electrically connected to each other. At the same time, the resin layer is melted and cured to form an encapsulating resin layer between the semiconductor elements and the wiring circuit boards so that the semiconductor elements are encapsulated. In this manner, a semiconductor device is produced.

Alternatively, the resin composition for semiconductor encapsulation is supplied onto a matrix wiring circuit board having individual wiring circuits formed thereon to form a resin layer. The wiring circuit board having a resin layer formed thereon is then cut to individual wiring circuit boards. Subsequently, semiconductor elements each provided with a plurality of connecting electrode portions and the wiring circuit boards thus separated are contact-bonded to each other under heating with the connecting electrode portion side of the

semiconductor elements and the wiring circuit boards being opposed to each other so that they are electrically connected to each other. At the same time, the resin layer is melted and cured to form an encapsulating resin layer between the semiconductor elements and the wiring circuit boards so that the semiconductor elements are encapsulated. In this manner, a semiconductor device is produced.

As the method for forming the resin layer made of the resin composition for semiconductor encapsulation there may be used a method using a dispenser, a method involving printing through an opening in the mask or the like similarly to the method previously mentioned.

The present invention will be further described in the following examples and comparative examples, but the present invention should not be construed as being limited thereto.

(1) Examples and comparative examples of the first embodiment of the resin composition for semiconductor encapsulation (combination of a liquid epoxy resin and a solid phenolic resin) will be described hereinafter.

The following various components were prepared prior to these examples.

[Epoxy resin aa1]

Bisphenol F type epoxy resin (Epo Tohto YDF-8170, produced by Tohto Kasei Co., Ltd.; liquid at 25°C; epoxy equivalent: 158 g/eq)

[Epoxy resin aa2]

Bisphenol A type epoxy resin (DER-332, produced by The Dow Chemical Company; liquid at 25°C; epoxy equivalent: 170 g/eq)

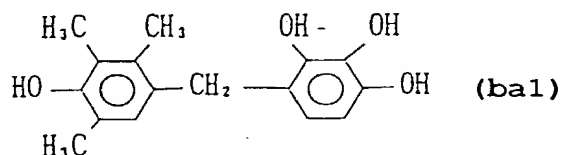
[Epoxy resin aa3]

Alicyclic epoxy resin (Celloxide 2021P, produced by DAISEL CHEMICAL INDUSTRIES, LTD; liquid at 25°C; epoxy equivalent: 135 g/eq)

5 [Phenolic resin ba1]

Tetrafunctional phenolic resin represented by the following chemical formula (ba1) (THD-2344, produced by Honshu Chemical Industry Co., Ltd.; solid at 25°C; melting point: 187°C; purity: 94%)

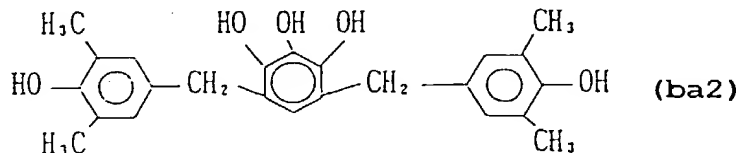
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15 [Phenolic resin ba2]

Pentafunctional phenolic resin represented by the following chemical formula (ba2) (BisPG-26X, produced by Honshu Chemical Industry Co., Ltd.; solid at 25°C; melting point: 182°C; purity: 97.5%)

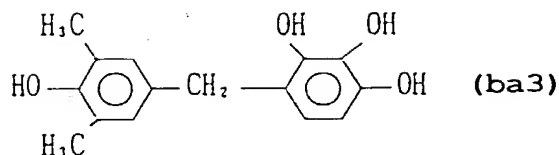
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25 [Phenolic resin ba3]

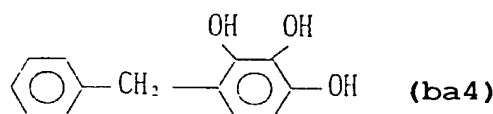
Tetrafunctional phenolic resin represented by the following chemical formula (ba3) (MHD-2344, produced by Honshu Chemical Industry Co., Ltd.; solid at 25°C; melting point: 156°C;

purity: 93.6%)



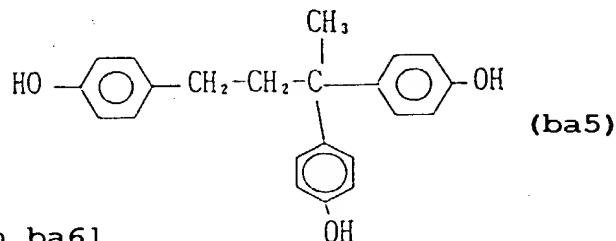
[Phenolic resin ba4]

Trifunctional phenolic resin represented by the following chemical formula (ba4) (HDM-234, produced by Honshu Chemical Industry Co., Ltd.; solid at 25°C; melting point: 94°C; purity: 98%)



[Phenolic resin ba5]

Solid phenolic resin represented by the following chemical formula (ba5) (TrisP-RK, produced by Honshu Chemical Industry Co., Ltd.; solid at 25°C; melting point: 95°C; purity: 89%)

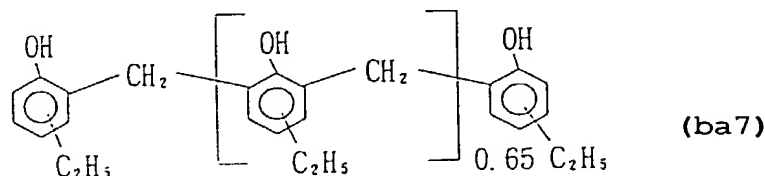


[Phenolic resin ba6]

Triphenylmethane type phenolic resin [MEH-7500 (3,4P), produced by Meiwa Kasei Co., Ltd.; solid at 25°C; hydroxyl equivalent: 101 g/eq; melting point: 110°C; 150°C viscosity: 3 to 4 poise]

[Phenolic resin ba7]

p-Ethylphenol resin represented by the following chemical formula (ba7) (produced by Meiwa Kasei Co., Ltd.; solid at 25°C; hydroxyl equivalent: 129 g/eq; melting point: 98°C)



[Phenolic resin ba8]

Allylated phenol novolak (MEH-8005H, produced by Meiwa Kasei Co., Ltd.; liquid at 25°C; hydroxyl equivalent: 135 g/eq)

[Acid anhydride-based curing agent]

Methylhexahydrophthalic anhydride (methylated HHPA, Rikacid MH-700, produced by New Japan Chemical Co., Ltd.)

[Curing accelerator cl]

A microcapsule type curing accelerator was prepared in accordance with the method previously mentioned. In some detail, 11 parts of an adduct of 3 mols of xylylene diisocyanate and 1 mol of trimethylolpropane and 4.6 parts of an adduct of 3 mols of tolylene diisocyanate and 1 mol of trimethylolpropane were uniformly dissolved in a mixture of 7 parts of triphenyl phosphine and 3.9 parts of ethyl acetate as curing accelerators to prepare an oil phase. Separately, an aqueous phase comprising 100 parts of distilled water and 5 parts of a polyvinyl alcohol was prepared. To the aqueous phase thus prepared was then added the oil phase

triphenylphosphine was 30% by weight based on the total amount of the microcapsule type curing accelerator.

[Curing accelerator c2]

MCE-9957 produced by NIPPON KAYAKU CO., LTD.

5 [Curing accelerator c3]

2-Ethyl-4-methylimidazole (Curezol 2E4MZ, produced by Shikoku Chemicals Corp.)

[Inorganic filler d1]

Spherical fused silica powder (SP-4B, produced by TONEN
10 CHEMICAL CORPORATION; average particle diameter: 5.0 μm)

[Inorganic filler d2]

Spherical fused silica powder (FB-48X, produced by DENKI
KAGAKU KOGYO K.K.; average particle diameter: 15 μm)

[Inorganic filler d3]

15 Spherical fused silica powder (SE-2100, produced by Admatechs Co., Ltd.; average particle diameter: 0.56 μm)

EXAMPLES a1 - a12; COMPARATIVE EXAMPLES a1 - a4; CONVENTIONAL
EXAMPLES

The foregoing various components were mixed in proportions
20 set forth in Tables 1 to 3 below, and then kneaded in a universal agitator so that they were melt-mixed. Subsequently, the mixture was cooled to room temperature to prepare the desired resin compositions for semiconductor encapsulation. The kneading conditions were as follows.

25 [EXAMPLES a1 - a8; COMPARATIVE EXAMPLE a4]

The epoxy resin and the phenolic resin were charged into the universal agitator where they were stirred at a temperature of 150°C for 2 minutes to dissolve all the solid contents.

Subsequently, the temperature of the solution was lowered to a range of from 90°C to 100°C. To the solution was then added the inorganic filler. The mixture was then stirred for 10 minutes. The mixture was then adjusted to a temperature of 75°C. To the mixture was then added the curing accelerator. The mixture was then stirred for 2 minutes for use.

[EXAMPLES a9 - a12; COMPARATIVE EXAMPLES a2, a3]

The epoxy resin and the phenolic resin were charged into the universal agitator where they were then stirred at a temperature of 110°C for 5 minutes to dissolve all the solid contents. Subsequently, the temperature of the solution was lowered to a range of from 90°C to 100°C. To the solution was then added the inorganic filler. The mixture was then stirred for 10 minutes. The mixture was then adjusted to a temperature of 65°C. To the mixture was then added the curing accelerator. The mixture was then stirred for 2 minutes for use.

[COMPARATIVE EXAMPLE a1; CONVENTIONAL EXAMPLE]

The epoxy resin, the phenolic resin or the acid anhydride were charged into the universal agitator where they were then stirred at a temperature of 80°C for 10 minutes. Thereafter, the mixture was stirred with the inorganic filler at the same temperature for 10 minutes. The temperature of the mixture was then lowered to 50°C. To the mixture was then added the curing accelerator. The mixture was then stirred for 2 minutes for use.

Table 1

(Unit: parts)

		Example No.					
		a1	a2	a3	a4	a5	a6
Epoxy resin	aa1	404	404	341	358	414	207
	aa2	-	-	-	-	-	-
	aa3	-	-	-	-	-	-
Phenolic resin	ba1	93.9	93.9	-	-	-	-
	ba2	93.9	93.9	77	80.7	93	46.5
	ba3	-	-	77	80.7	93	46.5
	ba4	-	-	-	-	-	-
	ba5	-	-	-	-	-	-
	ba6	-	-	-	-	-	-
	ba7	-	-	-	-	-	-
	ba8	-	-	-	-	-	-
Acid anhydride-based curing accelerator		-	-	-	-	-	-
Curing accelerator	c1	21.8	-	18.0	18.7	21.6	10.9
	c2	-	32.9	-	-	-	-
	c3	-	-	-	-	-	-
Inorganic filler	d1	140	140	1005	962	900	190
	d2	746	746	-	-	-	1010
Content of inorganic filler in total amount (wt-%)		59	59	66	64	59	79

Table 2

(Unit: parts)

		Example No.					
		a7	a8	a9	a10	a11	a12
Epoxy resin	aa1	311	825	-	366	-	285
	aa2	-	-	420	-	-	-
	aa3	-	-	-	-	300	-
Phenolic resin	ba1	-	-	-	-	-	-
	ba2	69.8	186	-	-	-	-
	ba3	69.8	186	-	-	-	-
	ba4	-	-	173	-	-	-
	ba5	-	-	-	-	-	301
	ba6	-	-	-	234	-	-
	ba7	-	-	-	-	287	-
	ba8	-	-	-	-	-	-
Acid anhydride-based curing accelerator		-	-	-	-	-	-
Curing accelerator	c1	16.4	43.5	20.0	27.4	33.5	35.2
	c2	-	-	-	-	-	-
	c3	-	-	-	-	-	-
Inorganic filler	d1	167	47.3	140	900	139	139
	d2	884	253	747	-	741	740
Content of inorganic filler in total amount (wt-%)		69	19	59	59	59	59

a height of 180 mm was put the resin composition for semiconductor encapsulation to a height of 120 mm. The test tube was then capped. The test specimen was allowed to stand at a temperature of 25°C for 30 days, and then confirmed for the degree of sedimentation of inorganic filler. In order to judge if sedimentation takes place or not, the change in turbidity was visually confirmed by making the use of the fact that where the inorganic filler is sedimented, the level of turbidity of the resin composition for semiconductor encapsulation changes.

When the turbidity decreases (transparency increases), it is defined that sedimentation takes place. In some detail, when the height of the sedimented portion is 1 mm or more, it is defined that sedimentation takes place. This state is represented by the mark X. When there is no sedimented portion, it is defined that no sedimentation takes place. This state is represented by the mark ⊙.

*2: Degree of viscosity change

The resin composition for semiconductor encapsulation was allowed to stand in a 25°C atmosphere for 30 days. The sample was measured for viscosity before and after aging by means of an E type viscometer (measuring temperature: 80°C (25°C for conventional example)). When the viscosity after aging is 1.5 times or less that before aging, it is judged as ⊙. When the viscosity after aging is from more than 1.5 times to 3.0 times or less that before aging, it is judged as ○. When the viscosity after aging is from more than 3.0 times to 10 times or less that before aging, it is judged as △. When the viscosity after aging is more than 10 times that before aging, it is judged as X. The

measurement of viscosity by means of an E type viscometer was effected in the same manner as the measurement of viscosity at 25°C or 80°C.

[Dischargeability, coatability]

5 For the evaluation of dischargeability, the amount of the resin composition for semiconductor encapsulation discharged through a dispenser under specific time and pressure conditions was determined. In some detail, using a 10 cc syringe produced by Musashi Engineering Co., Ltd. and a Type SN-17G metallic needle
10 (inner diameter: 2.4 mm), the discharged amount of the resin composition was measured after 10 seconds at 5 kg/cm². When the discharged amount is 1,000 mg or more, it is defined as ◎. When the discharged amount is from 200 mg or more to less than 1,000 mg or more, it is defined as ○. When the discharged amount
15 is from 50 mg or more to less than 200 mg or more, it is defined as △. When the discharged amount is less than 50 mg, it is defined as X. When the discharged amount is less than 50 mg, the level of encapsulation of semiconductor with resin is unacceptable.

20 [Pot life (viscosity change)]

The various resin compositions for semiconductor encapsulation were each measured for viscosity before and after aging at a temperature of 50°C for 72 hours by means of an E type viscometer (measuring temperature: 80°C (25°C for
25 conventional example)). When the viscosity after aging is 1.5 times or less that before aging, it is defined as ◎. When the viscosity after aging is from more than 1.5 times to 3.0 times or less that before aging, it is defined as ○. When the viscosity

after aging is from more than 3.0 times to 10 times or less that before aging, it is defined as Δ . When the viscosity after aging is more than 10 times that before aging, it is defined as X. The measurement of viscosity by means of an E type viscometer was effected in the same manner as the measurement of viscosity at 25°C or 80°C.

[Reliability in humidity resistance]

Semiconductor devices were produced from the various resin compositions for semiconductor encapsulation. In some detail, a dual in-line package (DIP) having semiconductor chips mounted thereon was set in a DIP mold. The mold was then heated to a temperature of 140°C (110°C for conventional example). The resin composition for semiconductor encapsulation which had previously been defoamed and melted at a temperature of from 60°C to 120°C was dropped onto the mold, and then cured at a temperature of 150°C for 3 hours (at a temperature of 110°C for 20 hours for conventional example) to produce a semiconductor device. The semiconductor device was then subjected to aging under humidity conditions of 130°C, 85%RH and 2.3 atm (applied bias voltage: 30 V). The various patterns were then checked for continuity. When the pattern is found discontinuous, it is defined as defective. The check for continuity was conducted on the sample which had been withdrawn to room temperature. The time at which the percent defective of package [(number of defective patterns/number of patterns) x 100] reaches 50% was then confirmed. Other conditions were as follows:

* Package evaluated: DIP16

* Lead frame: 4.2 alloy; thickness: 0.25 mm

- * Chip: All surface Al model element (3 x 6 mm),
produced by Shinko K.K. (Al pattern:
thickness: 5 μm ; width: 5 μm)
- * Wire: 99.99%; Gold SR 25 $\mu\text{m}\phi$ (diameter)
- * Ag paste: EN-4000, produced by Hitachi Chemical Co.,
Ltd.
- * Number evaluated (n): 10 packages/sample (one package
comprises two patterns connected to positive electrode
and two patterns connected to negative electrode,
giving a total of 40 measurable patterns)
- * PCT conditions: 130°C x 85%RH x 2.3 atm (applied
bias voltage: 30 V)

Table 4

		Example No.			
		a1	a2	a3	a4
Properties at 25°C		Semi-solid	Semi-solid	Semi-solid	Semi-solid
Viscosity (poise)	25°C	105000	104000	>300000	>300000
	80°C	25	24	4600	2500
Glass transition temp. (°C)		137	136	134	134
Storage stability	*1	◎	◎	◎	◎
	*2	◎	○	◎	◎
Dischargeability, coatability		◎	◎	○	◎
Pot life		◎	○	◎	◎
Reliability in humidity resistance (PCT)		200<	200<	200<	200<

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 5

		Example No.			
		a5	a6	a7	a8
Properties at 25°C		Semi-solid	Solid	Semi-solid	Semi-solid
Viscosity (poise)	25°C	17400	solid	161000	13200
	80°C	1300	3070	68	16
Glass transition temp. (°C)		135	134	135	136
Storage stability	*1	⊙	⊙	⊙	⊙
	*2	⊙	⊙	⊙	⊙
Dischargeability, coatability		⊙	○	⊙	⊙
Pot life		⊙	⊙	⊙	⊙
Reliability in humidity resistance (PCT)		200<	200<	200<	100

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 6

		Example No.			
		a9	a10	a11	a12
Properties at 25°C		Semi-solid	Solid	Semi-solid	Solid
Viscosity (poise)	25°C	9600	solid	11400	Solid
	80°C	10	160	25	328
Glass transition temp. (°C)		120	129	112	107
Storage stability	*1	◎	◎	◎	◎
	*2	◎	◎	○	◎
Dischargeability, coatability		◎	◎	◎	◎
Pot life		◎	◎	○	◎
Reliability in humidity resistance (PCT)		180	200<	90	180

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 7

		Comparative Example No.				Conven-ti onal example
		a1	a2	a3	a4	
Properties at 25°C		Liquid	Liquid	Semi-s olid	Solid	Liquid
Viscosity (poise)	25°C	5700	6500	11000	Solid	850
	80°C	15	33	15	5600	15
Glass transition temp. (°C)		59	126	130	133	140
Storage stability	*1	X	X	⊙	⊙	X
	*2	⊙	⊙	X	⊙	X
Dischargeability, coatability		⊙	⊙	⊙	X	⊙
Pot life		⊙	⊙	X	⊙	△
Reliability in humidity resistance (PCT)		70	190	60	190	20

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

5 As can be seen in the results of Table 4 to 7 above, all the products of the present examples show no sedimentation of inorganic filler and exhibit a prolonged pot life and better storage stability as compared with the product of the conventional example. The resin compositions for semiconductor encapsulation of the present examples exhibit an excellent dischargeability and coatability. The semiconductor devices produced from the resin compositions for semiconductor encapsulation of the present examples exhibit a good reliability in humidity resistance. In particular, the products of Examples 10 a1 to a9 comprise a polyfunctional solid phenolic resin as a solid phenolic resin and thus exhibit a higher glass transition temperature than those comprising a solid phenolic resin which 15

is not a polyfunctional solid phenolic resin. Further, the products of Examples a1 and a3 to a12 comprise a specific microcapsule type curing accelerator as a latent curing accelerator and thus exhibit a prolonged pot life and hence is excellent particularly in storage stability as compared with those comprising a commercially available microcapsule type curing accelerator.

On the contrary, the product of Comparative Example a1 comprises a liquid epoxy resin and a liquid phenolic resin and thus exhibits a deteriorated storage stability that causes the sedimentation of inorganic filler. Further, the product of Comparative Example a2 exhibits a viscosity of less than 7,000 poise at a temperature of 25°C and thus is liable to sedimentation of inorganic filler. Moreover, the product of Comparative Example a3 comprises a curing accelerator which is not a latent curing accelerator and thus exhibits a reduced pot life and hence an increased viscosity change during storage. Finally, the resin composition for semiconductor encapsulation of Comparative Example a4 exhibits a viscosity of more than 5,000 poise at a temperature of 80°C and hence a deteriorated dischargeability and coatability.

The present invention will be further described in the following examples of process for the production of the foregoing semiconductor devices.

EXAMPLE a13

The resin composition for semiconductor encapsulation prepared in Example a4 was previously defoamed at a temperature of 70°C in vacuo. The resin composition thus defoamed was then

used to produce a semiconductor device. In some detail, the resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 100°C through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

15 EXAMPLE a14

A semiconductor device was produced by flip chip mounting in the same manner as in Example a13 except that the resin composition for semiconductor encapsulation of Example a5 which had been previously defoamed at a temperature of 70°C in vacuo was used.

20 EXAMPLE a15

The resin composition for semiconductor encapsulation prepared in Example a9 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from the resin composition thus defoamed as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein the two components are electrically connected to each other with a bonding wire was prepared. The resin

A semiconductor device was produced by flip chip mounting in the same manner as in Example a13 except that the resin composition for semiconductor encapsulation of Example a10 which had been previously defoamed at a temperature of 70°C in vacuo was used.

25 The various components were mixed in proportions set forth in Table 8 below, melt-kneaded in a universal agitator, and then defoamed at a temperature of 70°C in vacuo. The mixture was then cooled to room temperature to prepare a desired resin

composition for semiconductor encapsulation. The kneading conditions were as follows. In some detail, the epoxy resin and the phenolic resin were charged into the universal agitator where they were then stirred at a temperature of 150°C for 2 minutes so that all the solid content was dissolved. Subsequently, the temperature in the universal agitator was lowered to a range of from 90°C to 100°C, and the mixture was then mixed with the inorganic filler for 10 minutes. Subsequently, the temperature in the universal agitator was adjusted to 75°C, and the mixture was then mixed with the curing accelerator for 2 minutes for use.

The resin composition for semiconductor encapsulation $\alpha 1$ thus obtained was then measured for viscosity at 25°C and 80°C by means of an E type viscometer in accordance with the methods previously mentioned. The resin composition for semiconductor encapsulation $\alpha 1$ was then measured and evaluated for glass transition temperature (T_g), storage stability (degree of sedimentation of inorganic filler, degree of change of viscosity), dischargeability, coat ability and pot life in accordance with the methods previously mentioned. The semiconductor device produced from the foregoing resin composition for semiconductor encapsulation was then measured and evaluated for reliability in humidity resistance in accordance with the method previously mentioned. The results are set forth in Table 8 below.

Table 8

		Resin composition for semiconductor encapsulation α1
Epoxy resin	aa1	414
Phenolic resin	ba2	93
	ba3	93
Curing accelerator	c1	21.6
Inorganic filler	d3	900
Content of inorganic filler in total amount (wt-%)		59
Properties at 25°C		Semi-solid
Viscosity (poise)	25°C	51,000
	80°C	17
Glass transition temperature (°C)		133
Storage stability	*1	◎
	*2	◎
Dischargeability and coatability		◎
Pot life		◎
Reliability in humidity resistance (PCT)		200 <

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

5 EXAMPLE a17

A semiconductor device was produced from the resin composition for semiconductor encapsulation α1 as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been previously heated to a temperature of 100°C was filled with the resin composition for semiconductor encapsulation α1 which had been previously heated to a temperature of 70°C through a dispenser. Thereafter, the resin composition for semiconductor

encapsulation α1 thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE a18

A semiconductor device was produced from the resin composition for semiconductor encapsulation α1 as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been previously heated to a temperature of 70°C was filled with the resin composition for semiconductor encapsulation α1 which had been previously heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser. Thereafter, the resin composition for semiconductor encapsulation α1 thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE a19

A semiconductor device was produced from the resin composition for semiconductor encapsulation α1 as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been

previously heated to a temperature of 70°C was filled with the resin composition for semiconductor encapsulation α1 which had been previously heated at a pressure of 5 Torr to a temperature of 70°C through a dispenser. Thereafter, the pressure was returned to atmospheric pressure. The gap between the wiring circuit board and the semiconductor elements was then filled with the resin composition for semiconductor encapsulation α1 which had been heated to a temperature of 70°C (differential pressure filling). The resin composition for semiconductor encapsulation α1 thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE a20

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 100°C through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap

between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE a21

5 A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had previously been
10 heated to a temperature of 100°C through a dispenser so that the semiconductor elements were potted. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were
15 then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner,
20 a semiconductor was produced by flip chip mounting.

EXAMPLE a22

 A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor
25 encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser so that the wiring circuit board was potted.

Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

10 EXAMPLE a22

A semiconductor device was produced from the resin composition for semiconductor encapsulation α1 as follows. In some detail, the resin composition for semiconductor encapsulation α1 which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE a23

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had been previously heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser so that the semiconductor elements were potted. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE a24

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had previously been heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor

elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions ($150^{\circ}\text{C} \times 2 \text{ kg} \times 20 \text{ min.} + 220^{\circ}\text{C} \times 0.5 \text{ kg} \times 2 \text{ min.}$), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE a25

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE a26

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C

was applied to semiconductor elements which had previously been heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE a27

A semiconductor device was produced from the resin composition for semiconductor encapsulation α1 as follows. In some detail, the resin composition for semiconductor encapsulation α1 which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, using a flip chip bonder, semiconductor elements which had been heated to a temperature of 70°C were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner,

a semiconductor was produced by flip chip mounting.

EXAMPLE a28

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 100°C through a dispenser so that the semiconductor elements were potted. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE a29

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C through a dispenser at a pressure of 5 Torr so that the semiconductor elements were potted. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C

for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

5 EXAMPLE a30

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, 10 the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

20 EXAMPLE a31

A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 1$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 25

The semiconductor device produced in Example a20 was placed on a mounting board (mother board), electrically connected to the mounting board, and then heated to a temperature of 100°C. Thereafter, the gap between the mounting board and the semiconductor device was filled with the resin composition for semiconductor encapsulation α1 which had been heated to a temperature of 70°C through a dispenser. The resin composition thus applied was then thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the semiconductor device and the mounting board. In this manner, a semiconductor product was produced.

The various semiconductor devices thus obtained and the foregoing product having a semiconductor device mounted on a mounting board were each visually examined for presence of bubbles in the encapsulating resin layer. The results are set forth in Tables 9 to 11.

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	Example No.		
	a31	a32	a33
Presence of bubbles	Small amount	None	Small amount

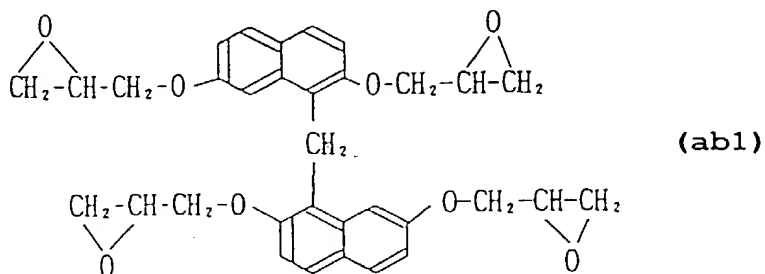
As can be seen in the results of Tables 9 to 11, all the semiconductor devices except those of Examples a28 and a30 were
5 observed to have no or a small amount of bubbles present in the encapsulating resin layer.

(2) Examples and comparative examples of the second embodiment of the resin composition for semiconductor encapsulation (combination of a solid epoxy resin and a liquid phenolic resin) will be described hereinafter.

The following various components were prepared prior to these examples.

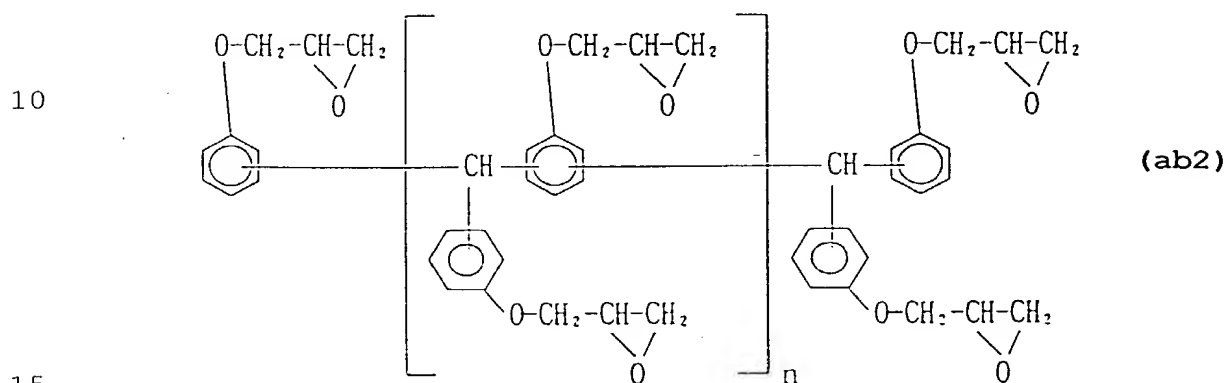
[Epoxy resin abl]

Tetrafunctional naphthalene type epoxy resin represented
15 by the following structural formula (ab1) (EXA-4701, produced
by DAINIPPON INK & CHEMICALS, INC.; solid at 25°C; epoxy
equivalent: 167 g/eq; softening point: 68°C)



[Epoxy resin ab2]

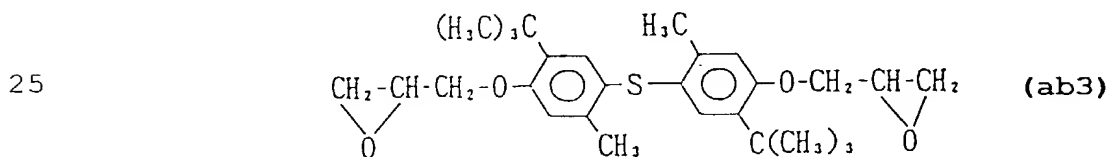
Triphenylmethane type epoxy resin represented by the following chemical formula (ab2) (EPPN-501HY, produced by NIPPON KAYAKU CO., LTD.; solid at 25°C; epoxy equivalent: 170 g/eq; softening point: 62°C)



wherein n represents a number of about 1.8.

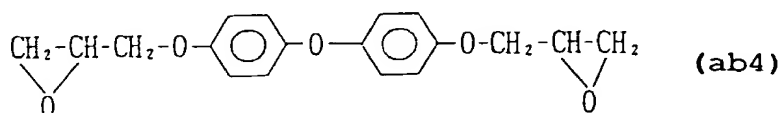
[Epoxy resin ab3]

Solid epoxy resin represented by the following structural formula (ab3) (GK-4292, produced by Nippon Steel Chemical Co., Ltd.; solid at 25°C; epoxy equivalent: 247 g/eq; melting point: 121°C)



[Epoxy resin ab4]

Solid epoxy resin represented by the following structural formula (ab4) (GK-4137, produced by Nippon Steel Chemical Co., Ltd.; solid at 25°C; epoxy equivalent: 174 g/eq; melting point: 79°C)

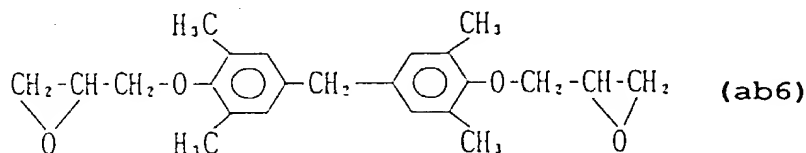


[Epoxy resin ab5]

Bisphenol F type epoxy resin [Epo Tohto YDF-8170, produced by Tohto Kasei Corp.; liquid at 25°C (13 poise); epoxy equivalent: 158 g/eq]

[Epoxy resin ab6]

Solid epoxy resin represented by the following structural formula (ab6) (GK-5079, produced by Nippon Steel Chemical Co., Ltd.; solid at 25°C; epoxy equivalent: 190 g/eq)



[Epoxy resin ab7]

Triglycidyl isocyanurate (TEPIC-S, produced by Nissan Chemical Industries, Ltd.; solid at 25°C; epoxy equivalent: 100

09914363 022701

[Phenolic resin bb1]

Allylated phenol novolak (MEH-8005H, produced by Showa Kasei Co., Ltd.; liquid at 25°C; hydroxyl equivalent: 135 g/eq)

5 [Phenolic resin bb2]

Diallylated bisphenol A (produced by Gunei Chemical Industry Co., Ltd.; liquid at 25°C; hydroxyl equivalent: 154 g/eq)

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[Curing accelerator c1]
```

10 The same microcapsule type curing accelerator as the curing
accelerator c1 used in the examples and comparative examples
of the first embodiment was used. The average particle diameter
of the microcapsule type curing accelerator was 2 μm . The ratio
of the shell thickness to the particle diameter of microcapsules
15 was 15%. The encapsulated amount of triphenylphosphine was 30%
by weight based on the total amount of the microcapsule type
curing accelerator.

```
[Curing accelerator c2]
```

MCE-9957 produced by NIPPON KAYAKU CO., LTD.

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20  [curing accelerator c3]
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2-Ethyl-4-methylimidazole (Curezol 2E4MZ, produced by Shikoku Chemicals Corp.)

[Inorganic filler d1]

Spherical fused silica powder (SP-4B, produced by TONEN
25 CHEMICAL CORPORATION; average particle diameter: 5.0 μ m)

[Inorganic filler d2]

Spherical fused silica powder (FB-48X, produced by DENKI KAGAKU KOGYO K.K.; average particle diameter: 15 μm)

[Inorganic filler d3]

Spherical fused silica powder (SE-2100, produced by Admatechs Co., Ltd.; average particle diameter: 0.56 μ m)

EXAMPLES b1 - b10; COMPARATIVE EXAMPLES b1 - b4

5 The foregoing various components were mixed in proportions set forth in Tables 12 to 14 below, and then kneaded in a universal agitator so that they were melt-mixed. Subsequently, the mixture was cooled to room temperature to prepare the desired resin compositions for semiconductor encapsulation. The
10 kneading conditions were as follows.

[EXAMPLES b1 - b4; COMPARATIVE EXAMPLES b3, b4]

The epoxy resin and the phenolic resin were charged into the universal agitator where they were stirred at a temperature of 110°C for 5 minutes to dissolve all the solid contents.

15 Subsequently, the temperature of the solution was lowered to a range of from 90°C to 100°C. To the solution was then added the inorganic filler. The mixture was then stirred for 10 minutes. The mixture was then adjusted to a temperature of 65°C. To the mixture was then added the curing accelerator. The mixture was
20 then stirred for 2 minutes for use.

[EXAMPLES b5 - b7; COMPARATIVE EXAMPLE b2]

The epoxy resin and the phenolic resin were charged into the universal agitator where they were stirred at a temperature of 130°C for 10 minutes to dissolve all the solid contents.

25 Subsequently, the temperature of the solution was lowered to a range of from 90°C to 100°C. To the solution was then added the inorganic filler. The mixture was then stirred for 10 minutes. The mixture was then adjusted to a temperature of 75°C. To the

mixture was then added the curing accelerator. The mixture was then stirred for 2 minutes for use.

[EXAMPLES b8 - b10]

The epoxy resin and the phenolic resin were charged into
 5 the universal agitator where they were stirred at a temperature
 of 150°C for 10 minutes. Subsequently, the temperature of the
 solution was lowered to a range of from 90°C to 100°C. To the
 solution was then added the inorganic filler. The mixture was
 then stirred for 10 minutes. The mixture was then adjusted to
 10 a temperature of 65°C. To the mixture was then added the curing
 accelerator. The mixture was then stirred for 2 minutes for
 use.

[COMPARATIVE EXAMPLE b1]

The epoxy resin and the phenolic resin were charged into
 15 the universal agitator where they were stirred at a temperature
 of 80°C for 10 minutes. Thereafter, to the solution was then
 added the inorganic filler at the same temperature. The mixture
 was then stirred for 10 minutes. The mixture was then adjusted
 to a temperature of 50°C. To the mixture was then added the
 20 curing accelerator. The mixture was then stirred for 2 minutes
 for use.

Table 14

(Unit: parts)

		Comparative Example No.			
		b1	b2	b3	b4
Epoxy resin	ab1	-	-	-	-
	ab2	-	-	328	-
	ab3	-	290	-	-
	ab4	-	-	-	-
	ab5	239	-	-	-
	ab6	-	-	-	-
	ab7	-	-	-	569
Phenolic resin	bb1	204	160	260	569
	bb2	-	-	-	-
Curing accelerator	c1	23.8	18.7	-	66.6
	c2	-	-	-	-
	c3	-	-	5.2	-
Inorganic filler	d1	164	166	140	45
	d2	869	884	742	240
Content of inorganic filler in total amount (wt-%)		69	69	60	19

The resin compositions for semiconductor encapsulation of examples and comparative examples thus obtained were each measured for viscosity at 25°C and 80°C by means of an E type viscometer in accordance with the foregoing methods. These resin compositions for semiconductor encapsulation were each then measured and evaluated for glass transition temperature (T_g), storage stability (degree of sedimentation of inorganic filler, degree of change of viscosity), dischargeability, coatability and pot life in accordance with the following methods.

The semiconductor devices produced from the foregoing resin compositions for semiconductor encapsulation were then measured and evaluated for reliability in humidity resistance in accordance with the following method. The results are set forth in Tables 15 to 17 below.

[Glass transition temperature (Tg)]

The glass transition temperature was determined in the same manner as in the first embodiment.

[Storage stability]

*1: Degree of sedimentation of inorganic filler

The degree of sedimentation of inorganic filler was determined in the same manner as in the first embodiment.

*2: Degree of viscosity change

The degree of viscosity change was determined in the same manner as in the first embodiment.

[Dischargeability, coatability]

The dischargeability and coatability were determined in the same manner as in the first embodiment.

[Pot life (viscosity change)]

The pot life was determined in the same manner as in the first embodiment.

[Reliability in humidity resistance]

5 The reliability in humidity resistance was determined in the same manner as in the first embodiment.

Table 15

		Example No.				
		b1	b2	b3	b4	b5
Properties at 25°C		Semi-solid	Semi-solid	Semi-solid	Semi-solid	Solid
Viscosity (poise)	25°C	267000	250000	>300000	>300000	Solid
	80°C	128	120	235	143	3000
Glass transition temp. (°C)		118	119	118	119	88
Storage stability	*1	◎	◎	◎	◎	◎
	*2	◎	○	◎	◎	◎
Dischargeability, coatability		◎	◎	◎	◎	○
Pot life		◎	○	◎	◎	◎
Reliability in humidity resistance (PCT)		200<	200<	200<	200<	140

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 16

		Example No.				
		b6	b7	b8	b9	b10
Properties at 25°C		Semi-solid	Semi-solid	Semi-solid	Semi-solid	Solid
Viscosity (poise)	25°C	Solid	Solid	7500	Solid	Solid
	80°C	3510	4500	20	15	16
Glass transition temp. (°C)		87	88	71	65	66
Storage stability	*1	⊙	⊙	⊙	⊙	⊙
	*2	⊙	⊙	⊙	⊙	○
Dischargeability, coatability		○	△	⊙	⊙	⊙
Pot life		⊙	⊙	⊙	⊙	○
Reliability in humidity resistance (PCT)		140	150	150	60	60

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 17

		Comparative Example No.			
		b1	b2	b3	b4
Properties at 25°C		Liquid	Solid	Semi-solid	Liquid
Viscosity (poise)	25°C	5700	Solid	>300000	6500
	80°C	15	5600	250	15
Glass transition temp. (°C)		59	87	137	*3
Storage stability	*1	X	⊙	⊙	X
	*2	⊙	⊙	X	○
Dischargeability, coatability		⊙	X	⊙	⊙
Pot life		⊙	⊙	X	○
Reliability in humidity resistance (PCT)		70	100	80	*3

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

*3: Uncured and immeasurable

5 As can be seen in the results of Table 15 to 17 above, all the products of the present examples show no sedimentation of inorganic filler and exhibit a prolonged pot life and excellent storage stability. The resin compositions for semiconductor encapsulation of the present examples exhibit an excellent

10 dischargeability and coatability. The semiconductor devices produced from the resin compositions for semiconductor encapsulation of the present examples exhibit a good reliability in humidity resistance. In particular, the products of Examples b1 to b4 comprise a polyfunctional solid epoxy resin as a solid

15 epoxy resin and thus exhibit a higher glass transition temperature than those comprising a solid epoxy resin which is not a polyfunctional solid epoxy resin. Further, the products of Examples b1 and b3 to b9 comprise a specific microcapsule

On the contrary, the products of Comparative Examples b1 and b4 are a liquid which exhibits a viscosity of less than 7,000 poise at 25°C and thus exhibit a deteriorated storage stability that causes the sedimentation of inorganic filler. Further, the product of Comparative Example b2 exhibits a viscosity of more than 5,000 poise at a temperature of 80°C and thus exhibits a deteriorated fluidity that causes the deterioration of dischargeability and coatability. Moreover, the product of Comparative Example b3 comprises a curing accelerator which is not a latent curing accelerator and thus exhibits a reduced pot life and hence a drastically increased viscosity change during storage.

20 EXAMPLE b11

and semiconductor elements which had previously been heated to a temperature of 70°C at a pressure of 5 Torr by printing through an opening in the mask. Subsequently, the pressure of the atmosphere was raised to 50 Torr to remove voids contained in the resin composition for semiconductor encapsulation thus applied. The resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was then used to finish printing with the pressure of the atmosphere being kept to 50 Torr. Thereafter, the resin composition for semiconductor encapsulation thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements are encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b12

A semiconductor device was produced from the resin composition for semiconductor encapsulation prepared in Example b3 as follows. In some detail, the resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer

was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE b13

5 A semiconductor device was then produced from the resin composition for semiconductor encapsulation prepared in Example b4 as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein the two components are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was then applied to the wiring circuit board and semiconductor elements which had previously been heated to a temperature of 70°C at a pressure of 5 Torr by printing through an opening in the mask. Subsequently, the pressure of the atmosphere was raised to 50 Torr to remove voids contained in the resin composition for semiconductor encapsulation thus applied. The resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was then used to finish printing with the pressure of the atmosphere being kept to 50 Torr. Thereafter, the resin composition for semiconductor encapsulation thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements are encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b14

A semiconductor device was produced by flip chip mounting

in the same manner as in Example b13 except that the resin composition for semiconductor encapsulation prepared in Example b8 was used.

[Preparation of resin composition for semiconductor
5 encapsulation $\alpha 2$]

The various components were mixed in proportions set forth in Table 18 below, and then melt-kneaded in a universal agitator. The mixture was then cooled to room temperature to prepare a desired resin composition for semiconductor encapsulation. The
10 kneading conditions were as follows. In some detail, the epoxy resin and the phenolic resin were charged into the universal agitator where they were then stirred at a temperature of 110°C for 5 minutes so that all the solid content was dissolved. Subsequently, the temperature in the universal agitator was
15 lowered to a range of from 90°C to 100°C, and the mixture was then mixed with the inorganic filler for 10 minutes. Subsequently, the temperature in the universal agitator was adjusted to 65°C, and the mixture was then mixed with the curing accelerator for 2 minutes for use.

20 The resin composition for semiconductor encapsulation $\alpha 2$ thus obtained was then measured for viscosity at 25°C and 80°C by means of an E type viscometer in accordance with the methods previously mentioned. The resin composition for semiconductor encapsulation $\alpha 2$ was then measured and evaluated for glass
25 transition temperature (T_g), storage stability (degree of sedimentation of inorganic filler, degree of change of viscosity), dischargeability, coatability and pot life in accordance with the methods previously mentioned. The semiconductor device

produced from the foregoing resin composition for semiconductor encapsulation $\alpha 2$ was then measured and evaluated for reliability in humidity resistance in accordance with the method previously mentioned. The results are set forth in Table 18 below.

5 Table 18

		Resin composition for semiconductor encapsulation $\alpha 2$
Epoxy resin	ab1	325
Phenolic resin	bb1	263
Curing accelerator	c1	30.5
Inorganic filler	d3	882
Content of inorganic filler in total amount (wt-%)		59
Properties at 25°C		Semi-solid
Viscosity (poise)	25°C	150,000
	80°C	60
Glass transition temperature (°C)		121
Storage stability	*1	⊙
	*2	⊙
Dischargeability and coatability		⊙
Pot life		⊙
Reliability in humidity resistance (PCT)		200 <

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

EXAMPLE b15

10 A semiconductor device was produced from the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the

15 wiring circuit board and semiconductor elements which had been previously heated to a temperature of 70°C was filled with the

resin composition for semiconductor encapsulation $\alpha 2$ which had been previously heated to a temperature of 70°C through a dispenser. Thereafter, the resin composition for semiconductor encapsulation $\alpha 2$ thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE b16

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been previously heated to a temperature of 70°C was filled with the resin composition for semiconductor encapsulation $\alpha 2$ which had been previously heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser. Thereafter, the resin composition for semiconductor encapsulation $\alpha 2$ thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE b17

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements

mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been previously heated to a temperature of 70°C was filled with the resin composition for semiconductor encapsulation $\alpha 2$ which had been previously heated at a pressure of 5 Torr to a temperature of 70°C through a dispenser. Thereafter, the pressure was returned to atmospheric pressure. The gap between the wiring circuit board and the semiconductor elements was then filled with the resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C (differential pressure filling). The resin composition for semiconductor encapsulation $\alpha 2$ thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE b18

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting

heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE b21

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had been previously heated to a temperature of 90°C at a pressure of 5 Torr through a dispenser so that the semiconductor elements were potted. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip

chip mounting.

EXAMPLE b22

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 1$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had previously been heated to a temperature of 90°C by printing through an opening in the mask. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE b23

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then

electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE b24

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had previously been heated to a temperature of 90°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE b25

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C

was applied to a wiring circuit board which had previously been heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE b26

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C through a dispenser so that the semiconductor elements were potted. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b27

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C through a dispenser at a pressure of 5 Torr so that the semiconductor elements were potted. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b28

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were

encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b29

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b30

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 2$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Subsequently, the pressure of the atmosphere was

raised to 50 Torr to remove voids contained in the resin composition for semiconductor encapsulation $\alpha 2$ thus applied. The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was then used to finish printing with the pressure of the atmosphere being kept to 50 Torr. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE b31

The resin composition for semiconductor encapsulation $\alpha 2$ which had been heated to a temperature of 70°C was applied to a mounting board (mother board) through a dispenser so that the mounting board was potted. The semiconductor device produced in Example b20 was placed on the mounting board. Using an IR reflow furnace (conditions: 240°C x 10 sec), the semiconductor device and the mounting board were then electrically connected to each other with connecting electrode portions, and at the same time, an encapsulating resin layer was formed in the gap between the semiconductor device and the mounting board. In this manner, a semiconductor product was produced.

The various semiconductor devices thus obtained and the foregoing product having a semiconductor device mounted on a mounting board were each visually examined for presence of bubbles in the encapsulating resin layer. The results are set forth in Tables 19 to 21.

Table 19

Presence of bubbles	Example No.								
	b11	b12	b13	b14	b15	b16	b17	b18	b19
	Small amount	Small amount	Small amount	Small amount	Small amount	None	None	Small amount	Small amount

Table 20

Presence of bubbles	Example No.							
	b20	b21	b22	b23	b24	b25	b26	b28
	None	None	Small amount	Small amount	None	None	Yes	Yes
							Small amount	Small amount

00004363 002701

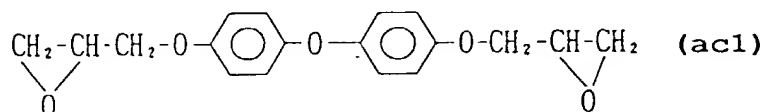
	Example No.		
	b29	b30	b31
Presence of bubbles	Small amount	None	Small amount

(3) Examples and comparative examples of the third embodiment of the resin composition for semiconductor encapsulation (combination of a solid epoxy resin and a solid phenolic resin) will be described hereinafter.

The following various components were prepared prior to these examples.

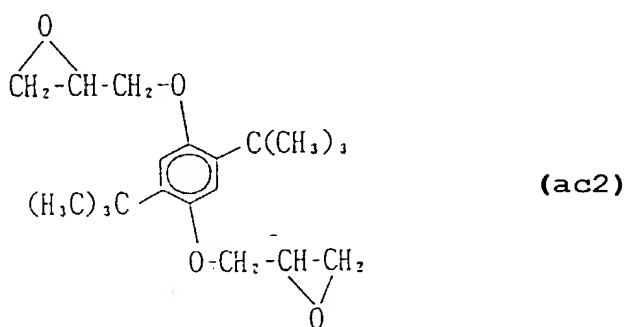
[Epoxy resin ac1]

Crystalline epoxy resin represented by the following chemical formula (ac1) [GK-4137, produced by Nippon Steel Chemical Co., Ltd.; solid at 25°C; epoxy equivalent: 174 g/eq; melting point: 79°C)



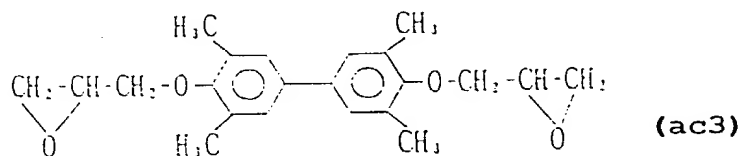
[Epoxy resin ac2]

Crystalline epoxy resin represented by the following chemical formula (ac2) (YDC-1312, produced by Tohto Kasei Co., Ltd.; solid at 25°C; epoxy equivalent: 173 g/eq; melting point: 145°C)



[Epoxy resin ac3]

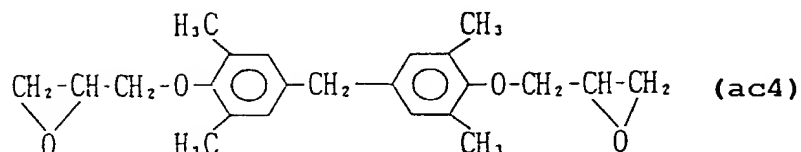
15 Crystalline epoxy resin represented by the following chemical formula (ac3) (YX-4000H, produced by Yuka Shell Co., Ltd.; solid at 25°C; epoxy equivalent: 195 g/eq; melting point: 105°C)



[Epoxy resin ac4]

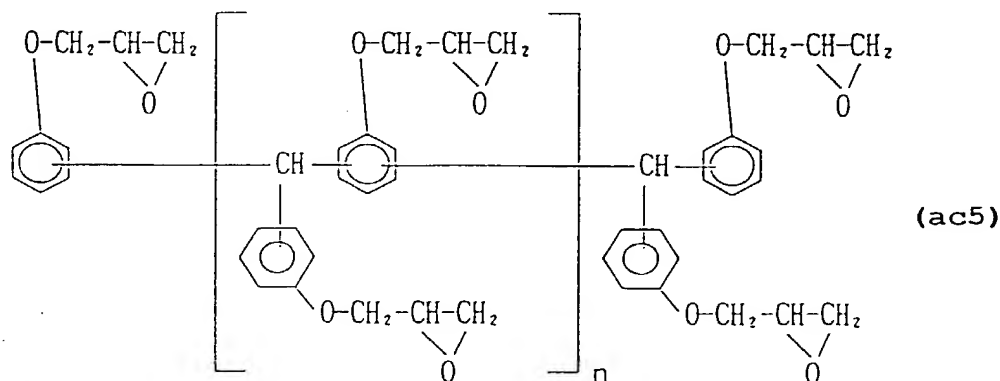
Solid epoxy resin represented by the following structural

formula (ab4) (GK-5079, produced by Nippon Steel Chemical Co., Ltd.; solid at 25°C; epoxy equivalent: 190 g/eq; melting point: 78°C)



[Epoxy resin ac5]

10 Triphenylmethane type epoxy resin represented by the following chemical formula (ac5) (EPPN-501HY, produced by NIPPON KAYAKU CO., LTD.; solid at 25°C; epoxy equivalent: 170 g/eq; softening point: 62°C)



wherein n represents a number of about 1.8.

[Epoxy resin ac6]

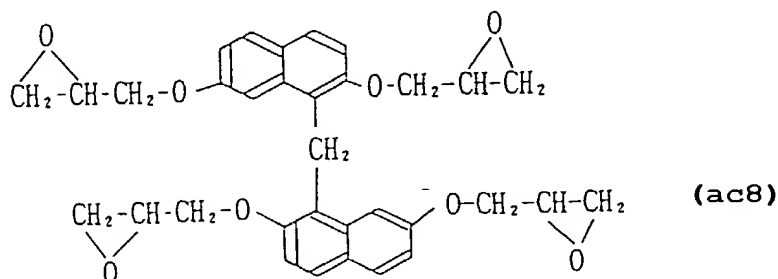
25 Bisphenol F type epoxy resin (Epo Tohto YDF-8170, produced by Tohto Kasei Co., Ltd.; liquid at 25°C; epoxy equivalent: 158 g/eq)

[Epoxy resin ac7]

Bisphenol A type epoxy resin (DER-332, produced by The

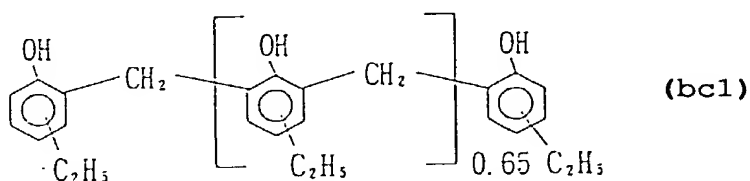
Dow Chemical Company; liquid at 25°C; epoxy equivalent: 170 g/eq)
[Epoxy resin ac8]

Tetrafunctional naphthalene type epoxy resin represented
by the following structural formula (ac8) (EXA-4701, produced
5 by DAINIPPON INK & CHEMICALS, INC.; solid at 25°C; epoxy
equivalent: 167 g/eq; softening point: 68°C)



15 [Phenolic resin bc1]

p-Ethylphenol resin represented by the following chemical
formula (bc1) (produced by Meiwa Kasei Co., Ltd.; solid at 25°C;
hydroxyl equivalent: 129 g/eq; melting point: 98°C)

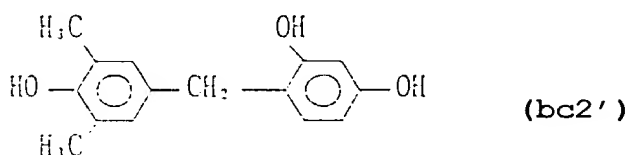


25 [Phenolic resin bc2]

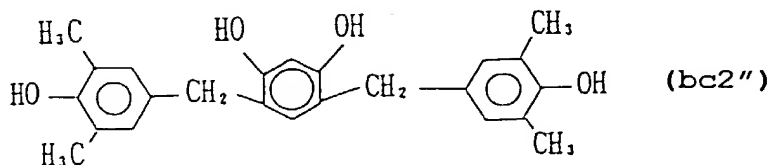
A mixture of a trifunctional phenolic resin represented
by the following chemical formula (bc2') and a tetrafunctional
phenolic resin represented by the following chemical formula

(bc2'') [MHD-244HG, produced by Honshu Chemical Industry Co., Ltd.; melting point: 140°C]. The peak area ratio of the resin represented by the chemical formula (bc2') and the resin represented by the chemical formula (bc2'') (to all peak area) are about 84% and about 15%, respectively, as analyzed by liquid chromatography.

10



15



[Phenolic resin bc3]

20

A mixture of the tetrafunctional phenolic resin represented by the chemical formula (bc2'') and the trifunctional phenolic resin represented by the following chemical formula (bc2') [Bis26X-RS, produced by Honshu Chemical Industry Co., Ltd.; melting point: 182°C]. The peak area ratio of the resin represented by the chemical formula (bc2'') and the resin represented by the chemical formula (bc2') (to all peak area) are about 90% and about 8%, respectively, as analyzed by liquid chromatography.

25

of the microcapsule type curing accelerator was 2 μm . The ratio of the shell thickness to the particle diameter of microcapsules was 15%. The encapsulated amount of triphenylphosphine was 30% by weight based on the total amount of the microcapsule type curing accelerator.

[Curing accelerator c2]

MCE-9957 produced by NIPPON KAYAKU CO., LTD.

[curing accelerator c3]

2-Ethyl-4-methylimidazole (Curezol 2E4MZ, produced by Shikoku Chemicals Corp.)

[Inorganic filler d1]

Spherical fused silica powder (SP-4B, produced by TONEN CHEMICAL CORPORATION; average particle diameter: 5.0 μm)

[Inorganic filler d2]

Spherical fused silica powder (FB-48X, produced by DENKI KAGAKU KOGYO K.K.; average particle diameter: 15 μm)

[Inorganic filler d3]

Spherical fused silica powder (SE-2100, produced by Admatechs Co., Ltd.; average particle diameter: 0.56 μm)

EXAMPLES c1 - c11; COMPARATIVE EXAMPLES c1 - c4

The foregoing various components were mixed in proportions set forth in Tables 22 to 24 below, and then kneaded in a universal agitator so that they were melt-mixed. Subsequently, the mixture was cooled to room temperature to prepare the desired resin compositions for semiconductor encapsulation. The kneading conditions were as follows.

[EXAMPLES c1 - c11; COMPARATIVE EXAMPLES c2 - c4]

The epoxy resin and the phenolic resin were charged into the universal agitator where they were stirred at a temperature of 150°C for 10 minutes to dissolve all the solid contents. Subsequently, the temperature of the solution was lowered to a range of from 90°C to 100°C. To the solution was then added the inorganic filler. The mixture was then stirred for 10 minutes. The mixture was then adjusted to a temperature of 75°C. To the mixture was then added the curing accelerator. The mixture was then stirred for 2 minutes for use. However, in Comparative Example 4, the mixture was adjusted to a temperature of 65°C, followed by the addition of the curing accelerator.

[COMPARATIVE EXAMPLE c1]

The epoxy resin and the phenolic resin were charged into the universal agitator where they were stirred at a temperature of 80°C for 10 minutes. Thereafter, to the mixture was added the inorganic filler at the same temperature. The mixture was then stirred for 10 minutes. The temperature of the solution was lowered to 50°C. To the solution was then added the inorganic filler. The mixture was then stirred for 2 minutes.

Table 23

(Unit: parts)

		Example No.					
		c6	c7	c8	c9	c10	c11
Epoxy resin	ac1	-	-	663	663	210	-
	ac2	134	-	-	-	-	-
	ac3	151	-	-	-	-	-
	ac4	147	-	-	-	229	-
	ac5	-	379	-	-	-	-
	ac6	-	-	-	-	-	-
	ac7	-	-	-	-	-	210
	ac8	-	-	-	-	-	163
Phenolic resin	bc1	-	-	491	491	311	-
	bc2	-	-	-	-	-	-
	bc3	-	-	-	-	-	-
	bc4	167	-	-	-	-	86.5
	bc5	-	221	-	-	-	-
	bc6	-	-	-	-	-	131
Curing accelerator	c1	19.5	25.9	57.5	-	36.4	25.3
	c2	-	-	-	85.9	-	-
	c3	-	-	-	-	-	-
Inorganic filler	d1	900	900	45.4	45.4	119	140
	d2	-	-	243	243	631	747
Content of inorganic filler in total amount (wt-%)		59	59	19	19	49	59

Table 24

(Unit: parts)

		Comparative Example No.			
		c1	c2	c3	c4
Epoxy resin	ac1	-	231	294	-
	ac2	-	-	-	-
	ac3	-	-	-	-
	ac4	-	-	-	-
	ac5	-	252	-	379
	ac6	239	-	-	-
Phenolic resin	bc1	-	342	-	-
	bc2	-	-	142	-
	bc3	-	-	134	-
	bc4	-	-	-	-
	bc5	-	-	-	221
	bc6	204	-	-	-
Curing accelerator	c1	23.8	40	32.3	-
	c2	-	-	-	-
	c3	-	-	-	4.4
Inorganic filler	d1	164	107	147	900
	d2	869	568	783	-
Content of inorganic filler in total amount (wt-%)		60	44	61	60

The resin compositions for semiconductor encapsulation of examples and comparative examples thus obtained were each measured for viscosity at 25°C and 80°C by means of an E type viscometer in accordance with the foregoing methods. These resin compositions for semiconductor encapsulation were each then measured and evaluated for glass transition temperature (T_g), storage stability (degree of sedimentation of inorganic filler, degree of change of viscosity), dischargeability, coatability and pot life in accordance with the following methods.

The semiconductor devices produced using the foregoing resin compositions for semiconductor encapsulation were then measured and evaluated for reliability in humidity resistance in accordance with the following method. The results are set forth in Tables 25 to 27 below.

[Glass transition temperature (Tg)]

The glass transition temperature was determined in the same manner as in the first embodiment.

[Storage stability]

*1: Degree of sedimentation of inorganic filler

The degree of sedimentation of inorganic filler was determined in the same manner as in the first embodiment.

*2: Degree of viscosity change

The degree of viscosity change was determined in the same manner as in the first embodiment.

[Dischargeability, coatability]

The dischargeability and coatability were determined in the same manner as in the first embodiment.

[Pot life (viscosity change)]

The pot life was determined in the same manner as in the first embodiment.

[Reliability in humidity resistance]

5 The reliability in humidity resistance was determined in the same manner as in the first embodiment.

Table 25

		Example No.				
		c1	c2	c3	c4	c5
Properties at 25°C		Solid	Solid	Solid	Solid	Semi-solid
Viscosity (poise)	25°C	Solid	Solid	Solid	Solid	255000
	80°C	22.5	24	4500	3000	82.5
Glass transition temp. (°C)		126	124	125	125	133
Storage stability	*1	◎	◎	◎	◎	◎
	*2	◎	○	◎	◎	◎
Dischargeability, coatability		◎	◎	△	○	◎
Pot life		◎	○	◎	◎	◎
Reliability in humidity resistance (PCT)		200<	200<	170	150	200<

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 26

		Example No.				
		c6	c7	c8	c9	c10
Properties at 25°C		Semi-solid	Solid	Solid	Solid	Semi-solid
Viscosity (poise)	25°C	50000	Solid	Solid	Solid	7200
	80°C	90	610	22.5	23	60
Glass transition temp. (°C)		134	136	88	85	94
Storage stability	*1	◎	◎	◎	◎	◎
	*2	◎	◎	◎	○	◎
Dischargeability, coatability		◎	○	◎	◎	◎
Pot life		◎	◎	◎	○	◎
Reliability in humidity resistance (PCT)		200<	200<	100	100	180

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

Table 27

		Example No.	Comparative Example No.			
		C11	c1	c2	c3	c4
Properties at 25°C		Semi-solid	Liquid	Liquid	Solid	Solid
Viscosity (poise)	25°C	50000	5700	6100	Solid	Solid
	80°C	36	15	55	5560	1500
Glass transition temp. (°C)		118	59	93	124	145
Storage stability	*1	⊙	X	X	⊙	⊙
	*2	⊙	⊙	⊙	⊙	X
Dischargeability, coatability		⊙	⊙	⊙	X	△
Pot life		⊙	⊙	⊙	⊙	X
Reliability in humidity resistance (PCT)		200	70	90	170	80

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

As can be seen in the results of Table 25 to 27 above,

5 all the products of the present examples show no sedimentation of inorganic filler and exhibit a prolonged pot life and excellent storage stability. The resin compositions for semiconductor encapsulation of the present examples exhibit an excellent dischargeability and coatability. The semiconductor devices

10 produced using the resin compositions for semiconductor encapsulation of the present examples exhibit a good reliability in humidity resistance. In particular, the products of Examples c1 to c7 comprise a polyfunctional solid phenolic resin as a solid phenolic resin and thus exhibit a higher glass transition

15 temperature than those comprising a solid resin which is not a polyfunctional solid resin. Further, the products of Examples c1, c3 to c8, c10 and c11 comprise a specific microcapsule type

On the contrary, the product of Comparative Example c1 comprises a liquid epoxy resin and a liquid phenolic resin and thus exhibits a deteriorated storage stability that causes the sedimentation of inorganic filler. Further, the product of Comparative Example c2 exhibits a viscosity of less than 7,000 poise at a temperature of 25°C and thus exhibits a deteriorated storage stability that causes the sedimentation of inorganic filler. Moreover, the product of Comparative Example c3 exhibits a viscosity of more than 5,000 poise that deteriorates the dischargeability and coatability thereof. The product of Comparative Example c4 comprises a curing accelerator which is not a latent curing accelerator and thus exhibits a reduced pot life and hence a deteriorated storage stability.

The present invention will be further described in the following examples of process for the production of the foregoing
20 semiconductor devices.

EXAMPLE c12

for semiconductor encapsulation which had been heated to a temperature of 70°C was then applied to the wiring circuit board and semiconductor elements which had previously been heated to a temperature of 70°C at a pressure of 5 Torr by printing through an opening in the mask. Subsequently, the pressure of the atmosphere was raised to 150 Torr to remove voids contained in the resin composition for semiconductor encapsulation thus applied. The resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was then used to finish printing with the pressure of the atmosphere being kept to 150 Torr. Thereafter, the resin composition for semiconductor encapsulation thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements are encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE c13

The resin composition for semiconductor encapsulation prepared in Example c4 was previously defoamed at a temperature of 70°C in vacuo. The resin composition thus defoamed was then used to produce a semiconductor device. In some detail, the resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 100°C through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically

connected to each other with connecting electrode portions under hot pressing conditions ($150^{\circ}\text{C} \times 2 \text{ kg} \times 20 \text{ min.} + 220^{\circ}\text{C} \times 0.5 \text{ kg} \times 2 \text{ min.}$), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE c14

A semiconductor device was produced by flip chip mounting in the same manner as in Example c12 except that the resin composition for semiconductor encapsulation of Example c6 which had been previously defoamed at a temperature of 70°C in vacuo was used.

EXAMPLE c15

A semiconductor device was produced by flip chip mounting in the same manner as in Example c12 except that the resin composition for semiconductor encapsulation of Example c7 which had been previously defoamed at a temperature of 70°C in vacuo was used.

EXAMPLE c16 (comprising epoxy resin and phenolic resin in combination)

The resin composition for semiconductor encapsulation prepared in Example c11 was previously defoamed at a temperature of 70°C in vacuo. The resin composition thus defoamed was then used to produce a semiconductor device. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein the two components are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation which had been heated to a

temperature of 70°C was then applied to the wiring circuit board and semiconductor elements which had previously been heated to a temperature of 70°C at a pressure of 5 Torr by printing through an opening in the mask. Subsequently, the pressure of the atmosphere was raised to 150 Torr to remove voids contained in the resin composition for semiconductor encapsulation thus applied. The resin composition for semiconductor encapsulation which had been heated to a temperature of 70°C was then used to finish printing with the pressure of the atmosphere being kept to 150 Torr. Thereafter, the resin composition for semiconductor encapsulation thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements are encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

[Preparation of resin composition for semiconductor encapsulation α3]

The various components were mixed in proportions set forth in Table 28 below, melt-kneaded in a universal agitator, and then defoamed at a temperature of 70°C in vacuo. The mixture was then cooled to room temperature to prepare a desired resin composition for semiconductor encapsulation. The kneading conditions were as follows. In some detail, the epoxy resin and the phenolic resin were charged into the universal agitator where they were then stirred at a temperature of 150°C for 10 minutes so that all the solid content was dissolved. Subsequently, the temperature in the universal agitator was lowered to a range of from 90°C to 100°C, and the mixture was

then mixed with the inorganic filler for 10 minutes. Subsequently, the temperature in the universal agitator was adjusted to 75°C, and the mixture was then mixed with the curing accelerator for 2 minutes for use.

5 The resin composition for semiconductor encapsulation $\alpha 3$ thus obtained was then measured for viscosity at 25°C and 80°C by means of an E type viscometer in accordance with the methods previously mentioned. The resin composition for semiconductor encapsulation $\alpha 3$ was then measured and evaluated for glass
10 transition temperature (T_g), storage stability (degree of sedimentation of inorganic filler, degree of change of viscosity), dischargeability, coatability and pot life in accordance with the methods previously mentioned. The semiconductor device produced using the foregoing resin composition for semiconductor
15 encapsulation $\alpha 3$ was then measured and evaluated for reliability in humidity resistance in accordance with the method previously mentioned. The results are set forth in Table 28 below.

Table 28

		Resin composition for semiconductor encapsulation $\alpha 3$
Epoxy resin	ac1	391
Phenolic resin	bc2	99.7
	bc3	99.7
Curing accelerator	c1	23.1
Inorganic filler	d3	886
Content of inorganic filler in total amount (wt-%)		59
Properties at 25°C		Solid
Viscosity (poise)	25°C	Solid
	80°C	25
Glass transition temperature (°C)		125
Storage stability	*1	◎
	*2	◎
Dischargeability and coatability		◎
Pot life		◎
Reliability in humidity resistance (PCT)		200 <

*1: Degree of sedimentation of inorganic filler

*2: Degree of viscosity change

5 EXAMPLE c17

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been previously heated to a temperature of 100°C was filled with the resin composition for semiconductor encapsulation $\alpha 3$ which had been previously heated to a temperature of 70°C through a dispenser. Thereafter, the resin composition for semiconductor

encapsulation $\alpha 3$ thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE c18

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been previously heated to a temperature of 70°C was filled with the resin composition for semiconductor encapsulation $\alpha 3$ which had been previously heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser. Thereafter, the resin composition for semiconductor encapsulation $\alpha 3$ thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE c19

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon with connecting electrode portions interposed therebetween was prepared. Subsequently, the gap between the wiring circuit board and semiconductor elements which had been

previously heated to a temperature of 70°C was filled with the resin composition for semiconductor encapsulation α 3 which had been previously heated at a pressure of 5 Torr to a temperature of 70°C through a dispenser. Thereafter, the pressure was returned to atmospheric pressure. The gap between the wiring circuit board and the semiconductor elements was then filled with the resin composition for semiconductor encapsulation α 3 which had been heated to a temperature of 70°C (differential pressure filling). The resin composition for semiconductor encapsulation α 3 thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor device was produced by flip chip mounting.

EXAMPLE c20

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 100°C through a dispenser so that the wiring circuit board was potted. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap

between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE c21

5 A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had previously been
10 heated to a temperature of 100°C through a dispenser so that the semiconductor elements were potted. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were
15 then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner,
20 a semiconductor was produced by flip chip mounting.

EXAMPLE c22

 A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, the resin composition for semiconductor
25 encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser so that the wiring circuit board was potted.

Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

10 EXAMPLE c23

A semiconductor device was produced using the resin composition for semiconductor encapsulation α3 as follows. In some detail, the resin composition for semiconductor encapsulation α3 which had been heated to a temperature of 70°C was applied to semiconductor elements which had been previously heated to a temperature of 70°C at a pressure of 5 Torr through a dispenser so that the semiconductor elements were potted. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE c24

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was applied to semiconductor elements which had previously been heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, using a flip chip bonder, a wiring circuit board which had been heated to a temperature of 70°C was placed on the semiconductor elements, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE c25

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, the resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was applied to a wiring circuit board which had previously been heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min.

in the mask at a pressure of 5 Torr. Thereafter, using a flip chip bonder, semiconductor elements were placed on the wiring circuit board, the semiconductor elements and the wiring circuit board were then electrically connected to each other with connecting electrode portions under hot pressing conditions (150°C x 2 kg x 20 min. + 220°C x 0.5 kg x 2 min.), and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. In this manner, a semiconductor was produced by flip chip mounting.

EXAMPLE c28

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 100°C through a dispenser so that the semiconductor elements were potted. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE c29

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In

some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C through a dispenser at a pressure of 5 Torr so that the semiconductor elements were potted. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE c30

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE c31

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE c32

A semiconductor device was produced using the resin composition for semiconductor encapsulation $\alpha 3$ as follows. In some detail, a wiring circuit board having semiconductor elements mounted thereon wherein they are electrically connected to each other with a bonding wire was prepared. The resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was then applied to the semiconductor elements which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Subsequently, the pressure of the atmosphere was raised to 150 Torr to remove voids contained in the resin composition for semiconductor encapsulation $\alpha 3$ thus applied.

The resin composition for semiconductor encapsulation $\alpha 3$ which had been heated to a temperature of 70°C was then used to finish printing with the pressure of the atmosphere being kept to 150 Torr. Thereafter, the resin composition thus applied was thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in such an arrangement that the semiconductor elements were encapsulated. In this manner, a cavity fill type of semiconductor device was produced.

EXAMPLE c33

The semiconductor device produced in Example c20 was placed on a mounting board (mother board), electrically connected to the mounting board, and then heated to a temperature of 100°C. The gap between the mounting board and the semiconductor device was then filled with the resin composition for semiconductor encapsulation α3 which had been heated to a temperature of 70°C through a dispenser. The resin composition thus applied was then thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer in the gap between the semiconductor device and the mounting board. In this manner, a semiconductor product was produced.

The various semiconductor devices and semiconductor products thus obtained were each visually examined for presence of bubbles in the encapsulating resin layer. The results are set forth in Tables 29 to 31.

of the resin layer. The semiconductor wafer was then cut into pieces by a dicer to produce semiconductor devices (wafer level SCP).

EXAMPLE d2

5 The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device (wafer level CSP) was then produced from the resin composition thus defoamed as follows. In some detail, the resin composition for semiconductor
10 encapsulation was applied to the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr to a thickness
15 of 150 μm . The resin composition thus applied was then thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer. Thereafter, the resin portion was polished in such a manner that the forward end of the protruding electrode portion was exposed to the exterior of the resin layer. The semiconductor
20 wafer was then cut into pieces by a dicer to produce semiconductor devices (wafer level SCP).

EXAMPLE d3

 The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature
25 of 70°C in vacuo. A semiconductor device (wafer level CSP) was then produced from the resin composition thus defoamed as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the protruding electrode portion

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EXAMPLE d4

A plurality of semiconductor elements were mounted on a matrix wiring circuit board having individual wiring circuits formed thereon with bonding wires. A semiconductor device (MAP-BGA process) was then produced from the resin composition for semiconductor encapsulation of Example a1 which had been defoamed at a temperature of 70°C as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the entire surface of semiconductor elements mounted on the matrix wiring circuit board which had been previously heated to a temperature of 70°C through a dispenser to form a resin in such an arrangement that the semiconductor elements were encapsulated. The resin layer thus formed was then defoamed

at a pressure of 5 Torr. The resin composition thus applied was then thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer. The matrix wiring circuit board was then cut into pieces by a dicer to produce semiconductor devices (MAP-BGA process).

EXAMPLE d5

A plurality of semiconductor elements were mounted directly on a matrix wiring circuit board having individual wiring circuits formed thereon. A semiconductor device (MAP-BGA process) was then produced from the resin composition for semiconductor encapsulation of Example a1 which had been defoamed at a temperature of 70°C as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the entire surface of semiconductor elements mounted on the matrix wiring circuit board which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. The resin composition thus applied was then thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer. The matrix wiring circuit board was then cut into pieces by a dicer to produce semiconductor devices (MAP-BGA process).

EXAMPLE d6

A plurality of semiconductor elements were mounted directly on a matrix wiring circuit board having individual wiring circuits formed thereon. A semiconductor device (MAP-BGA process) was then produced from the resin composition for semiconductor encapsulation of Example a1 which had been defoamed at a temperature of 70°C as follows. In some detail,

the resin composition for semiconductor encapsulation was applied to the entire surface of semiconductor elements mounted on the matrix wiring circuit board which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr. Thereafter, the pressure of the atmosphere was returned to 150 Torr where the resin composition thus applied was defoamed. Subsequently, the semiconductor wafer was subjected to finish printing at a pressure of 150 Torr. The resin composition thus applied was then thermoset at a temperature of 150°C for 3 hours to form an encapsulating resin layer. The matrix wiring circuit board was then cut into pieces by a dicer to produce semiconductor devices (MAP-BGA process).

EXAMPLE d7

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced using the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon which had been previously heated to a temperature of 70°C through a dispenser to a thickness of 50 μm to form a resin layer. Thereafter, the semiconductor wafer was cut into pieces by a dicer. Subsequently, using a flip chip bonder, a wiring circuit board which had been previously heated to a temperature of 70°C and the semiconductor elements thus

separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the wiring circuit board and the resin layer side of the semiconductor elements being opposed to each other. In this manner, the semiconductor elements and the wiring circuit board were electrically connected to each other with the protruding electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

EXAMPLE d8

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon which had been previously heated to a temperature of 70°C by printing through an opening in the mask to a thickness of 50 μ m. Thereafter, the semiconductor wafer was cut into pieces by a dicer. Subsequently, using a flip chip bonder, a wiring circuit board which had been previously heated to a temperature of 70°C and the semiconductor elements thus separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the wiring circuit board and the resin layer side of the semiconductor elements being opposed

to each other. In this manner, the semiconductor elements and the wiring circuit board were electrically connected to each other with the protruding electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

EXAMPLE d9

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon which had been previously heated to a temperature of 70°C by printing through an opening in the mask to a thickness of 50 μ m. The resin composition thus applied was then defoamed at a pressure of 5 Torr. Thereafter, the semiconductor wafer was cut into pieces by a dicer. Subsequently, using a flip chip bonder, a wiring circuit board which had been previously heated to a temperature of 70°C and the semiconductor elements thus separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the wiring circuit board and the resin layer side of the semiconductor elements being opposed to each other. In this manner, the semiconductor elements and the wiring circuit board were electrically connected

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from

the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements with protruding electrode portions formed thereon which had been previously heated to a temperature of 70°C by printing at a pressure of 5 Torr through an opening in the mask to a thickness of 50 μm. Thereafter, the pressure of the atmosphere was returned to 150 Torr where the resin composition thus applied was then defoamed.

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electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

5 EXAMPLE d11

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the entire surface of a matrix wiring circuit board having individual wiring circuits formed thereon which had been previously heated to a temperature of 70°C through a dispenser at a pressure of 5 Torr to form a resin layer. Thereafter, the matrix wiring circuit board was cut into pieces by a dicer. Subsequently, using a flip chip bonder, semiconductor elements each having a plurality of connecting electrode portions provided thereon and the wiring circuit boards thus separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the connecting electrode portion side of the semiconductor elements and the resin layer side of the wiring circuit boards being opposed to each other. In this manner, the semiconductor elements and the wiring circuit boards were electrically connected to each other with the protruding electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

EXAMPLE d12

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the entire surface of a matrix wiring circuit board having individual wiring circuits formed thereon which had been previously heated to a temperature of 70°C by printing through an opening in the mask to form a resin layer. Thereafter, the matrix wiring circuit board was cut into pieces by a dicer. Subsequently, using a flip chip bonder, semiconductor elements each having a plurality of connecting electrode portions provided thereon and the wiring circuit boards thus separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the connecting electrode portion side of the semiconductor elements and the resin layer side of the wiring circuit boards being opposed to each other. In this manner, the semiconductor elements and the wiring circuit boards were electrically connected to each other with the protruding electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

EXAMPLE d13

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from

the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for semiconductor encapsulation was applied to the entire surface of a matrix wiring circuit board having individual wiring circuits formed thereon which had been previously heated to a temperature of 70°C by printing through an opening in the mask to form a resin layer. The resin layer thus formed was then defoamed at a pressure of 5 Torr. Thereafter, the matrix wiring circuit board was cut into pieces by a dicer. Subsequently, using a flip chip bonder, semiconductor elements each having a plurality of connecting electrode portions provided thereon and the wiring circuit boards thus separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the connecting electrode portion side of the semiconductor elements and the resin layer side of the wiring circuit boards being opposed to each other. In this manner, the semiconductor elements and the wiring circuit boards were electrically connected to each other with the protruding electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit board and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

EXAMPLE d14

The resin composition for semiconductor encapsulation prepared in Example a1 was previously defoamed at a temperature of 70°C in vacuo. A semiconductor device was then produced from the resin composition thus defoamed by flip chip mounting as follows. In some detail, the resin composition for

semiconductor encapsulation was applied to the entire surface of a matrix wiring circuit board having individual wiring circuits formed thereon which had been previously heated to a temperature of 70°C by printing through an opening in the mask at a pressure of 5 Torr to form a resin layer. Thereafter, the pressure of the atmosphere was returned to 150 Torr where the resin composition thus applied was defoamed. Subsequently, the matrix wiring circuit board was subjected to finish printing at a pressure of 150 Torr. Thereafter, the matrix wiring circuit board was cut into pieces by a dicer. Subsequently, using a flip chip bonder, semiconductor elements each having a plurality of connecting electrode portions provided thereon and the wiring circuit boards thus separated were hot-pressed (conditions: 150°C x 2 kg x 20 min + 220°C x 0.5 kg x 2 min) with the connecting electrode portion side of the semiconductor elements and the resin layer side of the wiring circuit boards being opposed to each other. In this manner, the semiconductor elements and the wiring circuit boards were electrically connected to each other with the protruding electrode portion, and at the same time, an encapsulating resin layer was formed in the gap between the wiring circuit boards and the semiconductor elements. As a result, a semiconductor device was produced by flip chip mounting.

The various semiconductor devices thus obtained were each then visually observed for the presence of bubbles in the encapsulating resin layer. The results are set forth in Tables 32 and 33 below.

Table 32

Example No.									
	d1	d2	D3	d4	d5	d6	d7	d8	d9
Presence of bubbles	Small amount	Small amount	None	None	None	None	Small amount	Small amount	None

Table 33

Example No.				
	d10	d11	d12	d14
Presence of bubbles	None	None	Small amount	None

As can be seen in the results of Tables 32 and 33 above, all the semiconductor devices thus obtained were observed to have no or a small amount of bubbles present in the encapsulating resin layer.

5

INDUSTRIAL APPLICABILITY

As mentioned above, the present invention provides a resin composition for semiconductor encapsulation comprising an epoxy resin (component A), a phenolic resin (component B), a latent curing accelerator (component C) and an inorganic filler (component D) and having a viscosity at 25°C and 80°C falling within a predetermined range. Thus, the resin composition for semiconductor encapsulation according to the invention exhibits a prolonged pot life and an excellent storage stability as compared with the conventional liquid encapsulating medium. Further, the resin composition for semiconductor encapsulation according to the invention, even if it stays solid or semi-solid at room temperature, shows a rapid viscosity drop and thus can be liquefied at a temperature of as low as from 40°C to 80°C, showing an excellent dischargeability and coatability. In particular, since the resin composition for semiconductor encapsulation according to the invention stays solid or semi-solid at ordinary temperatures, it can be freely handled at ordinary temperatures even if it is not cured after applied to semiconductor elements or wiring circuit boards. Accordingly, a semiconductor wafer, matrix wiring circuit board or the like to which the resin composition for semiconductor encapsulation according to the invention has been applied can

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be cut into individual elements and wiring circuit boards which are then connected to each other by hot pressing method or the like using a flip chip bonder or the like. Further, since the resin composition for semiconductor encapsulation according to the invention can be stored solid or semi-solid at room temperature, the inorganic filler (component D) cannot be sedimented. Accordingly, the resin composition for semiconductor encapsulation according to the invention can be stored solid or semi-solid and then liquefied at low temperatures as necessary before use, making it possible to fairly encapsulate semiconductor elements. As a result, a semiconductor device having a high reliability particularly in humidity resistance can be obtained.

The resin composition for semiconductor encapsulation according to the invention is also advantageous in that it exhibits an extremely prolonged pot life and thus is excellent particularly in storage stability when it comprises as the latent curing accelerator (component C) a microcapsule type curing accelerator having a core-shell structure in which a core portion made of a curing accelerator is covered by a specific shell portion.

The resin composition for semiconductor encapsulation according to the invention is further advantageous in that it exhibits an excellent fluidity and thus is excellent particularly in dischargeability and coatability when it comprises as the inorganic filler (component D) a spherical fused silica powder incorporated therein in a predetermined proportion based thereon.

CLAIMS

1. A resin composition for semiconductor encapsulation comprising the following components (A) to (D) and having a viscosity of 7,000 poise or more at 25°C and 5,000 poise or less at 80°C:

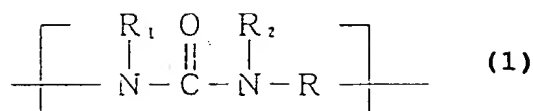
- (A) an epoxy resin;
- (B) a phenolic resin;
- (C) a latent curing accelerator; and
- (D) an inorganic filler.

2. The resin composition for semiconductor encapsulation according to Claim 1, wherein said component (A) is a liquid epoxy resin and said component (B) is a solid phenolic resin.

3. The resin composition for semiconductor encapsulation according to Claim 1, wherein said component (A) is a solid epoxy resin and said component (B) is a liquid phenolic resin.

4. The resin composition for semiconductor encapsulation according to Claim 1, wherein said component (A) is a solid epoxy resin and said component (B) is a solid phenolic resin.

5. The resin composition for semiconductor encapsulation according to any one of Claims 1 to 4, wherein said latent curing accelerator as the component (C) is a micro-capsule type curing accelerator having a core-shell structure comprising a core portion made of the curing accelerator encapsulated with a shell portion mainly composed of a polymer having a structural unit represented by the following general formula (1):



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wherein R represents a divalent or trivalent organic group; and R₁ and R₂, which may be the same or different, each represents a hydrogen atom or a monovalent organic group.

6. The resin composition for semiconductor encapsulation according to any one of Claims 1 to 5, wherein said inorganic filler as the component (D) is spherical fused silica powder and said spherical fused silica powder is incorporated in the resin composition for semiconductor encapsulation in a proportion of from 15 to 85% by weight based on the total amount of the resin composition for semiconductor encapsulation.

7. A semiconductor device comprising a semiconductor element provided on a wiring circuit board with a plurality of connecting electrode portions interposed therebetween, the gap between said wiring circuit board and said semiconductor element being sealed with an encapsulating resin layer, wherein said encapsulating resin layer is formed by a resin composition for semiconductor encapsulation according to any one of Claims 1 to 6.

8. A process for the production of a semiconductor device comprising a semiconductor element provided on a wiring circuit board with a plurality of connecting electrode portions interposed therebetween, the gap between said wiring circuit board and said semiconductor element being sealed with an

11. A semiconductor product comprising a semiconductor device having a resin encapsulation layer formed with a plurality of connecting electrode portions interposed therebetween, said semiconductor device being provided on a mounting board with the wiring circuit board of the semiconductor device being opposed to said mounting board, and the gap between said mounting board and said semiconductor device being sealed with an encapsulating resin layer, wherein said encapsulating resin layer is formed by a resin composition for semiconductor encapsulation according to any one of Claims 1 to 6.

12. A process for the production of a semiconductor device, which comprises a step of forming a resin layer made of a resin composition for semiconductor encapsulation according to any one of Claims 1 to 6 on the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements having protruding electrode portions formed thereon to a predetermined thickness in such an arrangement that at least the forward ends of said protruding electrode portions are exposed to the exterior of said resin layer, and a step of cutting said semiconductor wafer having a resin layer formed thereon to individual semiconductor elements.

13. The process for the production of a semiconductor device according to Claim 12, wherein said step of forming a resin layer is carried out by printing through an opening in a mask.

14. A process for the production of a semiconductor device, which comprises a step of supplying a resin composition for semiconductor encapsulation according to any one of Claims 1

to 6 onto the whole of a plurality of semiconductor elements provided on a matrix wiring circuit board having individual circuits formed thereon to form a resin layer with said semiconductor elements being incorporated therein, and a step
5 of cutting said matrix wiring circuit board having a resin layer formed thereon with said semiconductor elements being incorporated therein together with said resin layer to every semiconductor element.

15. The process for the production of a semiconductor
10 device according to Claim 14, wherein said step of forming a resin layer is carried out by printing through an opening in a mask.

16. A process for the production of a semiconductor device, which comprises a step of forming a resin layer made of a resin
15 composition for semiconductor encapsulation according to any one of Claims 1 to 6 on the protruding electrode portion side of a semiconductor wafer having a plurality of semiconductor elements having protruding electrode portions formed thereon to a predetermined thickness, a step of cutting said
20 semiconductor wafer having a resin layer formed thereon to individual semiconductor elements, and a step of contact-bonding a wiring circuit board and said semiconductor element thus cut under heating with the resin layer side of said semiconductor element and said wiring circuit board being opposed to each other
25 such that the two components are electrically connected to each other and said resin layer is melted and cured to form an encapsulating resin layer between said semiconductor element and said wiring circuit board, thereby encapsulating said

semiconductor element.

17. The process for the production of a semiconductor device according to Claim 16, wherein said step of forming a resin layer made of said resin composition for semiconductor encapsulation is carried out by printing through an opening in a mask.

18. The process for the production of a semiconductor device according to Claim 16, wherein said step of forming a resin layer made of said resin composition for semiconductor encapsulation is carried out by means of a dispenser.

19. A process for the production of a semiconductor device, which comprises a step of applying a resin composition for semiconductor encapsulation according to any one of Claims 1 to 6 to a matrix wiring circuit board having individual circuits formed thereon to form a resin layer thereon, a step of cutting said wiring circuit board having a resin layer formed thereon to individual wiring circuit boards, and a step of contact-bonding semiconductor elements each having a plurality of connecting electrode portions provided thereon and said wiring circuit boards thus cut under heating with the connecting electrode portion side of said semiconductor elements and said wiring circuit boards being opposed to each other such that the two components are electrically connected to each other and said resin layer is melted and cured to form an encapsulating resin layer between said semiconductor elements and said wiring circuit boards, thereby encapsulating said semiconductor element.

20. The process for the production of a semiconductor device according to Claim 19, wherein said step of applying a

ABSTRACT

The present invention provides a resin composition for semiconductor encapsulation excellent in reliability in humidity resistance and storage stability as well as in dischargeability and coatability. The resin composition for semiconductor encapsulation comprises: (A) an epoxy resin; (B) a phenolic resin; (C) a latent curing accelerator; and (D) an inorganic filler, and has a viscosity of 7,000 poise or more at 25°C and 5,000 poise or less at 80°C.

10

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

RESIN COMPOSITION FOR SEMICONDUCTOR ENCAPSULATION, SEMICONDUCTOR DEVICE COMPRISING THE SAME AND PROCESS FOR THE PRODUCTION OF SEMICONDUCTOR DEVICE USING THE SAME

the specification of which is attached hereto unless the following box is checked:

☐ was filed on _____ as United States Application Number or PCT International Application Number _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to the patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)			Priority Not Claimed
1. P. Hei. 11-048905	Japan	25/02/1999	<input type="checkbox"/>
2. P. Hei. 11-048906	Japan	25/02/1999	
3. P. Hei. 11-048907	(Country) Japan	(Day/Month/Year Filed) 25/02/1999	<input type="checkbox"/>
4. P. Hei. 11-212068	Japan	27/07/1999	
5. P. Hei. 11-212069	(Country) Japan	(Day/Month/Year Filed) 27/07/1999	<input type="checkbox"/>
6. P. Hei. 11-212070	Japan	27/07/1999	
7. P. Hei. 11-240511	(Country) Japan	(Day/Month/Year Filed) 26/08/1999	

I hereby claim the benefits under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below.

(Application Number)

(Filing Date)

I hereby claim the benefits under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

I hereby appoint John H. Mion, Reg. No. 18,879; Thomas J. Macpeak, Reg. No. 19,292; Robert J. Seas, Jr., Reg. No. 21,092; Darryl Mexic, Reg. No. 23,063; Robert V. Sloan, Reg. No. 22,776; Peter D. Olexy, Reg. No. 24,513; J. Frank Osha, Reg. No. 24,625; Waddell A. Biggart, Reg. No. 24,861; Louis Gubinsky, Reg. No. 24,835; Neil B. Siegel, Reg. No. 25,200; David J. Cushing, Reg. No. 28,703; John R. Inge, Reg. No. 26,916; Joseph J. Ruch, Jr., Reg. No. 26,577; Sheldon I. Landsman, Reg. No. 25,430; Richard C. Turner, Reg. No. 29,710; Howard L. Bernstein, Reg. No. 25,665; Alan J. Kasper, Reg. No. 25,426; Kenneth J. Burchfiel, Reg. No. 31,333; Gordon Kit, Reg. No. 30,764; Susan J. Mack, Reg. No. 30,951; Frank L. Bernstein, Reg. No. 31,484; Mark Boland, Reg. No. 32,197; William H. Mandir, Reg. No. 32,156; Scott M. Daniels, Reg. No. 32,562; Brian W. Hannon, Reg. No. 32,778; Abraham J. Rosner, Reg. No. 33,276; Bruce E. Kramer, Reg. No. 33,725; Paul F. Neils, Reg. No. 33,102; Brett S. Sylvester, Reg. No. 32,765 and Robert M. Masters, Reg. No. 35,603; my attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and request that all correspondence about the application be addressed to SUGHRUE, MION, ZINN, MACPEAK & SEAS, PLLC, 2100 Pennsylvania Avenue, N.W., Washington, D.C. 20037.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Date August 6, 2001

First Inventor Tadaaki

Harada

First Name

Last Name

Residence Osaka,

Signature Tadaaki

Harada

Japan

Post Office Address c/o NITTO DENKO CORPORATION, 1-2, Shimohozumi

Citizenship Japan

1-chome, Ibaraki-shi, Osaka 567-0041 Japan

Date August 6, 2001

Second Inventor Hideaki

Taki

First Name

Last Name

Residence Osaka,

Signature Hideaki

Taki

Japan

Post Office Address c/o NITTO DENKO CORPORATION, 1-2, Shimohozumi

Citizenship Japan

1-chome, Ibaraki-shi, Osaka 567-0041 Japan

00000000 00000000

3-00
Date August 6, 2001

Third Inventor Toshitsugu Hosokawa
First Name Last Name

Residence Osaka

Signature Toshitsugu Hosokawa

Japan JPx

Post Office Address c/o NITTO DENKO CORPORATION, 1-2, Shimohozumi

Citizenship Japan

1-chome, Ibaraki-shi, Osaka 567-0041 Japan

Date _____

Fourth Inventor _____

First Name

Last Name

Residence _____

Signature _____

Post Office Address _____

Citizenship _____

Date _____

Fifth Inventor _____

First Name

Last Name

Residence _____

Signature _____

Post Office Address _____

Citizenship _____

Date _____

Sixth Inventor _____

First Name

Last Name

Residence _____

Signature _____

Post Office Address _____

Citizenship _____

Date _____

Seventh Inventor _____

First Name

Last Name

Residence _____

Signature _____

Post Office Address _____

Citizenship _____

Date _____

Eighth Inventor _____

First Name

Last Name

Residence _____

Signature _____

Post Office Address _____

Citizenship _____

(Supply similar information for ninth and subsequent joint inventors.)